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SYSTEM ANALYSIS
FOR THE
HUNTSVILLE OPERATIONAL SUPPORT CENTER
DISTRIBUTED COMPUTER SYSTEM

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HUNTSVILLE OPERATIONAL SUPPORT CENTER
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MSSU-EIRS-EE-83-6
May 1982 - June 1983

Submitted by:

F. M. Ingels, Principal Investigator

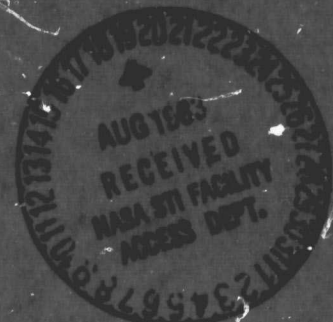
Mississippi State University
Electrical Engineering Department
Mississippi State, MS 39762
(601) 325-3912

Submitted to:

NASA/MSFC, Alabama
Technical Monitor: Frank Emmens, EB 32
(205) 453-4629

NAS8-34906

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SUMMARY

The Huntsville Operations Support Center (HOSC) is a distributed computer system used to provide real time data acquisition, analysis and display during NASA space missions and to perform simulation and study activities during non-mission times. The primary purpose of this research is to provide a HOSC system simulation model that may be used to investigate the effects of various HOSC system configurations. Such a model would be valuable in planning the future growth of HOSC and in ascertaining the effects of data rate variations, update table broadcasting and smart display terminal data requirements on the HOSC HYPER channel network system.

A simulation model was developed and programmed in three languages BASIC, PASCAL, and SLAM. Two of the programs are included in this report, the BASIC and the PASCAL language programs. SLAM is not supported by NASA/MSFC facilities and hence was not included. The statistical comparison of simulations of the same HOSC system configurations are in good agreement and are in agreement with the operational statistics of HOSC that were obtained.

Three variations of the most recent HOSC configuration have been run and some conclusions drawn as to the system performance

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under these variations. Section 3.4 discusses these results and conclusions.

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1.0 INTRODUCTION

1.1 HOSC System Overview

Marshall Space Flight Center (MSFC), Huntsville, Alabama, has implemented the Huntsville Operations Support Center (HOSC) to provide real time data acquisition, analysis, and display during NASA space missions. The HOSC is a distributed computer system composed of a network of large minicomputers and various peripheral equipment. Primarily designed to provide support for the Space Shuttle, Space Telescope, and Space Laboratory missions, the HOSC has the inherent flexibility to be expanded to meet the needs of future missions as well as providing MSFC with a large computer resource that can be used to support several non-mission activities.

The HOSC facility has been structured to include five large minicomputers and various peripheral equipment. The current network computers are each semi-dedicated to specific mission tasks (e.g. Space Shuttle Main Engine Data Analysis) and include three Perkin Elmer 3244 computers, a Perkin Elmer 8/32c computer, two DEC VAX 11/780 computers and a DEC 11/24 computer. An important role of the Perkin Elmer computers is acting as real time data receivers for mission data arriving via satellite and direct ground links from the Kennedy Space Center Firing Room at Cape Canaveral FL. These computers also act as a gateway to the network for the data which is needed by other mission activities supported by the other computers and peripherals. Peripheral equipment in the system includes two twelve channel Genisco Digital Television (D/TV's), strip recorders, and numerous unintelligent data terminals.

Foreseeable future expansion will include at least five more mini-computers, many more D/TV displays (possibly to 50), several more strip recorders, and intelligent data terminals.

The HOSC currently provides support for MSFC non-mission activities such as the Total POCC Preplanning Activity with future expansion providing data management resources for other non-mission activities. These activities might include the DEC IGDS (interactive graphics) and XEROX SIGMA (text processing) operations. All of these activities would be permitted use of the network resources through the Network Systems Corporation HYPER channel broadband local area network.

1.2 Scope of Report

In order to achieve the flexibility and efficiency needed by the HOSC, an analysis of the present system has been performed. This analysis coupled with projected system growth will insure that the HOSC remains a viable computing resource for MSFC. This report contains a summary of the baseline data gathered to begin the analysis of the HOSC computer network, Section 2.0, results of the analysis, Section 3.0, and a literature/bibliography Section 4.0. The report describes in detail some of the network components and also makes first iteration recommendations concerning network operations. This document should not be considered an end item since work still remains to be done in completely characterizing all the subtleties of the HOSC system.

1.3 Conclusion

From the work done thus far in the program, several conclusions and recommendations can be made.

A. Proposed IGDS/SIGMA Interface With HOSC

Network Systems Corporation does not currently produce hardware for the HYPER channel to XEROX processor interface. Consequently, a great amount of effort would be required to interface the SIGMA system directly with the HOSC HYPER channel.¹ A possible solution might be to interface the XEROX SIGMA to the network through a HYPER channel supported processor such as another DEC VAX. Feasibility of the VAX/XEROX interface has not been explored and may also present problems. A definite possibility to solve this problem is to develop a suitable software/hardware approach.

The DEC IGDS system interfaces with the HYPER channel and will present no obvious problems since the PDP-11 processor interface adapters are currently marketed by Network Systems Corporation.

B. CSO/HOSC Link Via HOSC HYPER Channel Adapter 4 For OI Data Exchange

The current plan is to interface CSO with HOSC using a separate trunk of adapter 4. By connecting the two installations with a separate trunk, CSO will be disallowed easy and immediate access to the HOSC resources on the HYPER channel. Because of the HYPER channel adapter design, direct trunk to trunk exchange of data is not possible. For trunk to trunk transfers, data from the initiating trunk must be channeled

through a processor on the common adapter and retransmitted by the processor over the other trunk.² If however, it is desirable to prevent CSO from easy access to the total HOSC resources, then the use of separate trunk is a good approach.

C. Summary of Analysis Activity

Progress on the analysis of HOSC has so far been steady but somewhat slow due to the difficulty in obtaining some needed baseline data. Below is a summary of the documentation accumulated to conduct the analysis effort.

. Perkin Elmer Corporation

3240 User's Manual 29-685

3240 Memory System Manual 29-688

8/32 User's Manual 29-394

8/32 Memory System Manual 29-428

(These manuals must contain the actual HOSC Computer internal DMA to I/O setup.)

. Network Systems Corporation

PI40 Peripheral Interface Manual

(Perkin Elmer I/O Bus Interface)

PI10 PI Manual or PI11 PI Manual

(Dependent on configuration of PDP-11 IGDS system: PI10 for DR11-B general purpose direct memory access or PI11 for DR70 MASSBUS interface.)

NETEX Software Documentation

. Marshall Space Flight Center

Completed system computer data rate flows.

D. Effects of Data File Dumps

It is desired to make large data file transfers on a periodic basis to refresh the data display terminals data base. This type of activity can create a log/jam effect on the most active data sources if the number of data bytes to be transferred are large enough to create waiting times. The basic relationship involves a tradeoff between the amount of storage of data by the data sources, their rate of data accumulation and the time required to transfer the data files.

This problem is discussed in Section 3.3 and 3.4 in detail.

2.0 HOSC SYSTEM DETAILS

The primary purpose of the Huntsville Operation Support Center is providing MSFC engineers with a near real time summary of vital information describing the operational status of certain components of the Space Shuttle during pre-launch and launch activities. This information allows MSFC engineers and contractor personnel to act in a support capacity to mission personnel at Kennedy Space Center (KSC), Cape Canaveral, Florida, and also Johnson Space Center (JSC), Houston, TX. MSFC support is provided by teams responsible for the Space Shuttle Main Engine (SSME), External Tanks (ET), Solid Rocket Boosters (SRB), Main Propulsion System (MPS) and the Range Safety System (RSS). Additional mission support is provided for various mission activities and programs that are the responsibility of MSFC personnel.

During powered flight, the HOSC will receive only data which is in the LPS (Launch Processing System) at KSC. The Shuttle support team will be in the HOSC during this phase of the mission and will be the point of contact with the JSC Mission Evaluation Room (MER) for problem discussion and resolution as required and will be on call during orbital operations. The Space Lab and experiment support team will be located in the HOSC during orbital operations when applicable.

Following completion of the active Shuttle vehicle support activities, data is recalled as required for more detailed analysis, and initial preparation is made to provide support to postflight evaluation.³

The HOSC is located in the west end of A-wing, building 4663 on Martin, Road, MSFC. Figures 1 and 2 show the functional components of the HOSC system and gives each component a referencing number that will be used in describing the system activities.

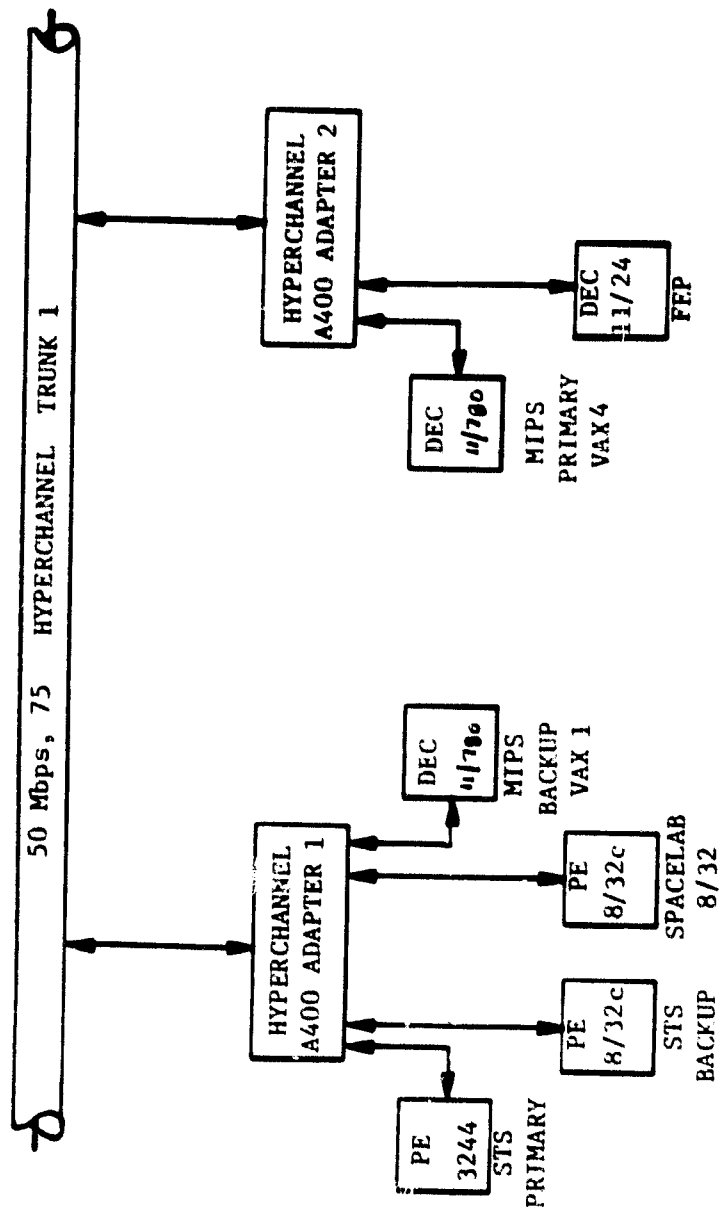


Figure 1. Original HOSC Hyper Channel Network Configuration

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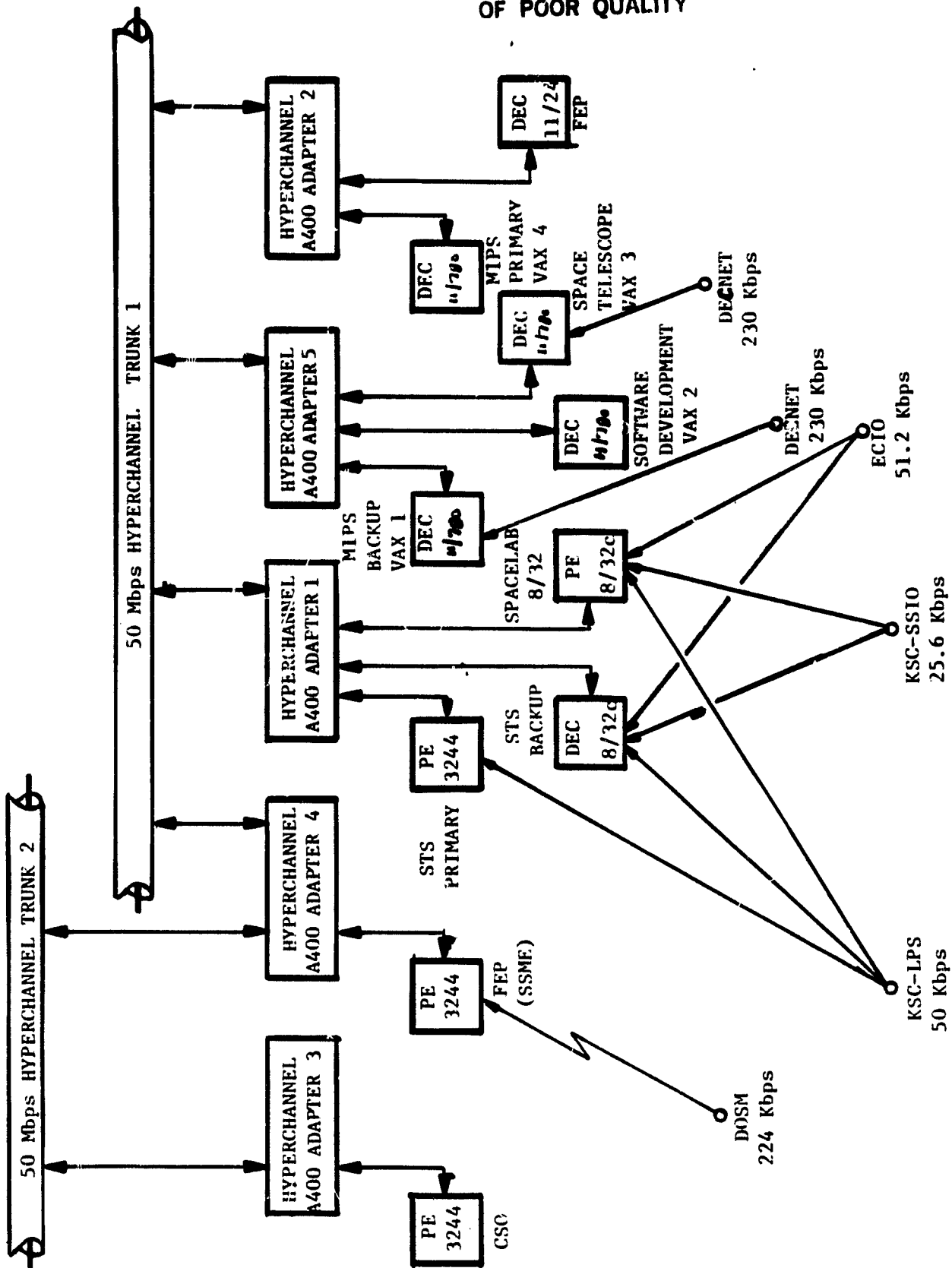


Figure 2. Proposed HYPER Channel Network Configuration

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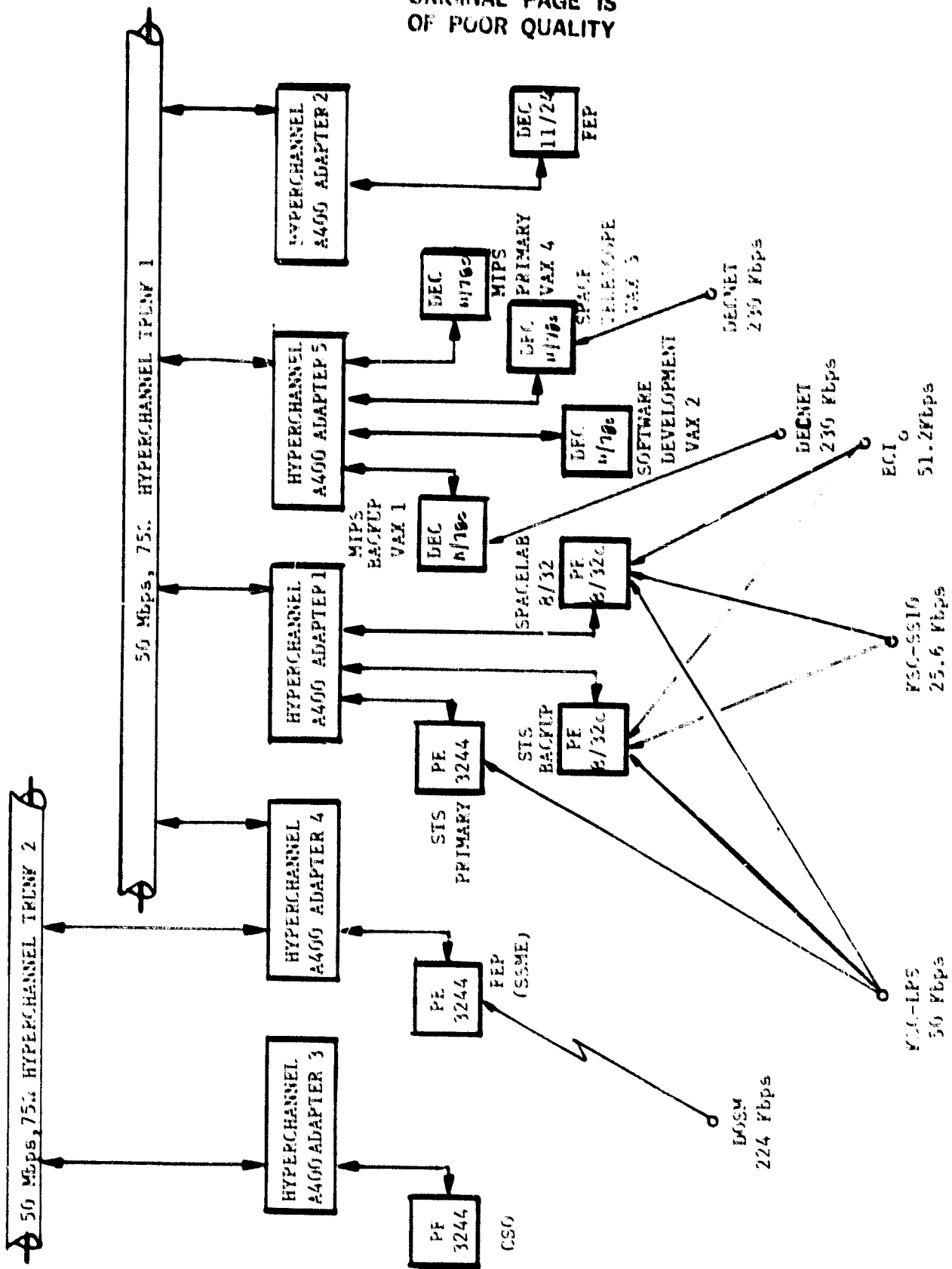


Figure 2a. Proposed FPP2 Channel Network Configuration Rev 1.
 (FPP2 Primary and 4 connected to Staples)

2.1 HOSC System Activities

In addition to the mission activities, the HOSC also provides support to several non-mission activities at MSFC. Details of all the HOSC activities are described below and summarized in Table 1.

2.1.1 Total POCC Preplanning

The POCC activity is an ongoing simulation activity for which the HOSC lends computer resources. This activity is in no way keyed to the real time mission activities and must be viewed as a continuous daily activity.

The POCC activity's impact on the HYPER channel network is basically that of continuous data transfers between the MIPS Primary Computer (VAX4, A400 Adapter 2) and the MIPS Backup Computer (VAX1, A400 Adapter 5). During each 24 hour period 150,000 512-byte blocks of data are transferred. Six times each day, an 8344 byte block is transferred (50,000 bytes cumulative). The remaining 100,000 512-byte blocks are transmitted randomly, but on an evenly distributed basis, throughout the day.

2.1.2 ECIO Data Stream

The POCC activity generates a continual 51.2 kilobit/second data stream known as the Experimental Computer Input/Output (ECIO) data stream. This data stream is ongoing and concurrent with the POCC activity. Data is transferred from MIPS Backup (VAX1, A400 Adapter 5) to the Spacelab 8/32 (PE 8/32c, A400 Adapter 1).

TABLE 1. HOSC DATA TRANSFERS

I. ROUTINE DAILY ACTIVITIES (Launch Independent)

A. Total POCC Preplanning Activity

Resources involved: MIPS Primary (VAX4, Adapter 2)
MIPS Backup (VAX1, Adapter 5)

Quantity of data: 150,000 512-byte blocks daily

B. ECIO Data Stream (Generated by POCC)

Resources involved: MIPS Backup (VAX1, Adapter 5)
Spacelab 8/32 (PE 8/32c, Adapter 1)

Quantity of data: 51.2 K bits/second concurrent with POCC.

C. IGDS/SIGMA Activity (Proposed)

Resources Involved: DEC IGDS and XEROX SIGMA and
communication with other resources
as needed.

Quantity of data: TBD

II. LAUNCH DAY ACTIVITIES:

A. Routine Daily Activities (See Above)B. Main Engine Data

Resources Involved: STS Primary (PE 3244, Adapter 1)
MIPS Backup (VAX1, Adapter 5)

Quantity of Data: 50 K bit/second stream (T-8 hours to T+12 minutes)

C. OI Data Stream

Resources Involved: FEB SSME (PE 3244, Adapter 4)
CSO Computers (Adapter 4)
STS Primary (PE 3244, Adapter 1)
MIPS Backup (VAX1, Adapter 5)

Quantity of Data: 128 K bit/second (T-9 sec to T+12 minutes)
into FEP and then to CSO. 40% will also
be transferred to STS and MIPS.

TABLE 1. HOSC DATA TRANSFERS (Continued)

D. Engineering Display Changes

Resources Involved: STS Primary (PE 3244, Adapter 1)
STS Backup (PE 8/32 Adapter 1)
Spacelab 8/32 (PE 8/32, Adapter 1)

Quantity of Data: Insignificant

2.1.3 Main Engine Data

Space Shuttle Main Engine data is collected and dissipated at the HOSC during a launch day activity only. Data is funneled through the HYPER channel network to MIPS Backup (VAX1, A400 Adapter 5) via STS Primary (PE 3244, A400 Adapter 1). STS Primary accepts a continual 50 kilobit per second data stream directly from the KSC firing room from 9 seconds before launch to 12 minutes after launch (MECO). Approximately 24 percent of this 50 Kb/s stream (12 Kb/s) is transferred over the network to MIPS Backup.

2.1.4 OI Data Stream

The OI data stream is a 128 kilobit per second data stream arriving at FEP SSME (PE 3244, A400 Adapter 4) on launch day only (t-9 seconds to T+12 minutes). This data will have a much greater future impact on the network than it does currently. The SSME computer acts as a front end processor for accepting this data stream from Goddard Space Flight Center and then writes the received data directly onto a magnetic tape for later transport to CSO. Later in the program this data will be shipped in its entirety over a separate HYPER channel trunk attached to A400 Adapter 4 to CSO. Additionally, about 40 percent of the data stream will be shipped over the HOSC network to supply and supplant the data currently being transferred by the Main Engine Data Activity.

2.1.5 Engineering Display Changes

This activity adds almost insignificantly to the total HYPER channel trunk traffic. The activity involves a transfer from STS Primary to STS Backup and the Spacelab 8/32 (PE3244 to two PE8/32's,

A400 Adapter, 1 only) of the name of each engineering console display format that is changed during the pre-launch and launch activities (T-9 seconds to T+12 minutes). This activity will be ignored in the HOSC system analysis due to its negligible contribution to total HYPER channel system traffic.

2.1.6 Proposed Activities

The most immediate proposed expansion of the HOSC network would allow two other non-mission activities access to the resources of the HOSC network. This activity would specify an additional A400 Adapter to allow resource sharing with the XEROX SIGMA system and the DEC PDP-11 IDGS system. Direct interface with the A400 is available for the PDP-11 but not for the XEROX system. A possible solution to allow the XEROX system access to the network through the A400 might be to use a compatible computer such as a DEC VAX 11/780 as a front end processor for the XEROX system. This activity is incompletely specified and will not affect the immediate analysis of the HOSC system.

2.2 HOSC System Components

The heart of the HOSC system is the Network Systems Corporation HYPER channel. The HYPER channel is a high speed digital communications facility that is used for interconnection of computer resources in a computing installation. The following sections describe the computer resources of the HOSC and how they are interconnected using the HYPER channel.

2.2.1 Computer Resources

2.2.1.1 DEC VAX 11/780

The HOSC makes use of Digital Equipment Corporation's VAX 11/780 computer as computational devices. The system currently includes two VAX computers (VAX1 and VAX4) designated as MIPS Primary and MIPS Backup. Future expansion will add two other VAX computers (VAX2 and VAX3) designated as Software Development and Space Telescope.

VAX computers support a 32-bit word architecture that is designed to aid in system throughput. Data transfers are accomplished via a 32-bit high speed data structure. This structure ties together the central processor, main memory, the UNIBUS subsystem, the MASS BUS subsystem and the DR780 high speed direct memory access subsystem. The 32-bit word architecture of the VAX establishes a virtual address space of 4.3 billion bytes of user addressable memory. A conceptual diagram of the VAX 11/780 bus structure is shown in Figures 3 and 4.

The Synchronous Backplane Interface (SBI) is the data path that links the central processor, the memory subsystem and the hardware adapters provided for the UNIBUS and MASSBUS. When interfaced to the SBI, the memory subsystem, the central processor, and the I/O controllers are known as NEXUSs.

All NEXUSs receive every SBI transfer. Logic in each NEXUS determines whether the NEXUS is the designated receiver for this transfer. Data transfers can occur from

CPU to memory subsystem

I/O controller to memory subsystem

CPU to I/O controllers.

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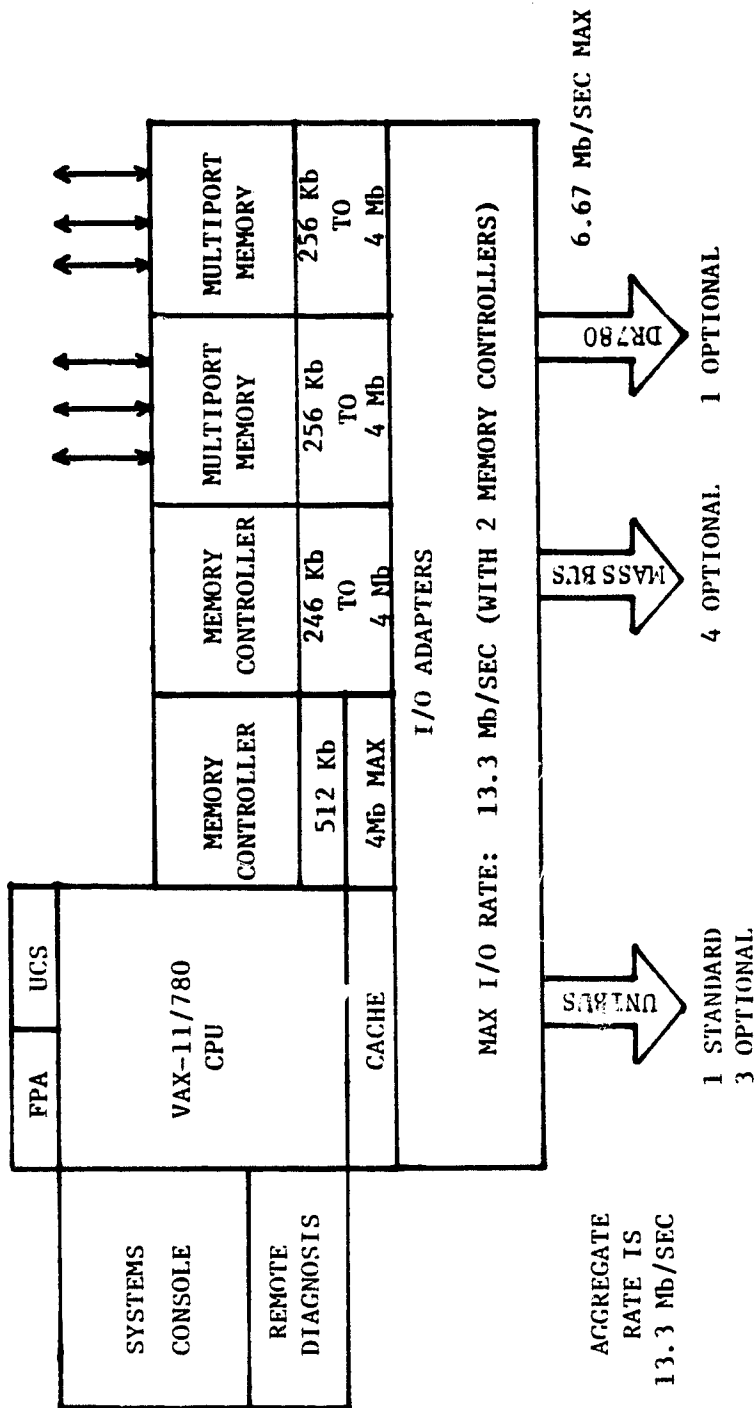
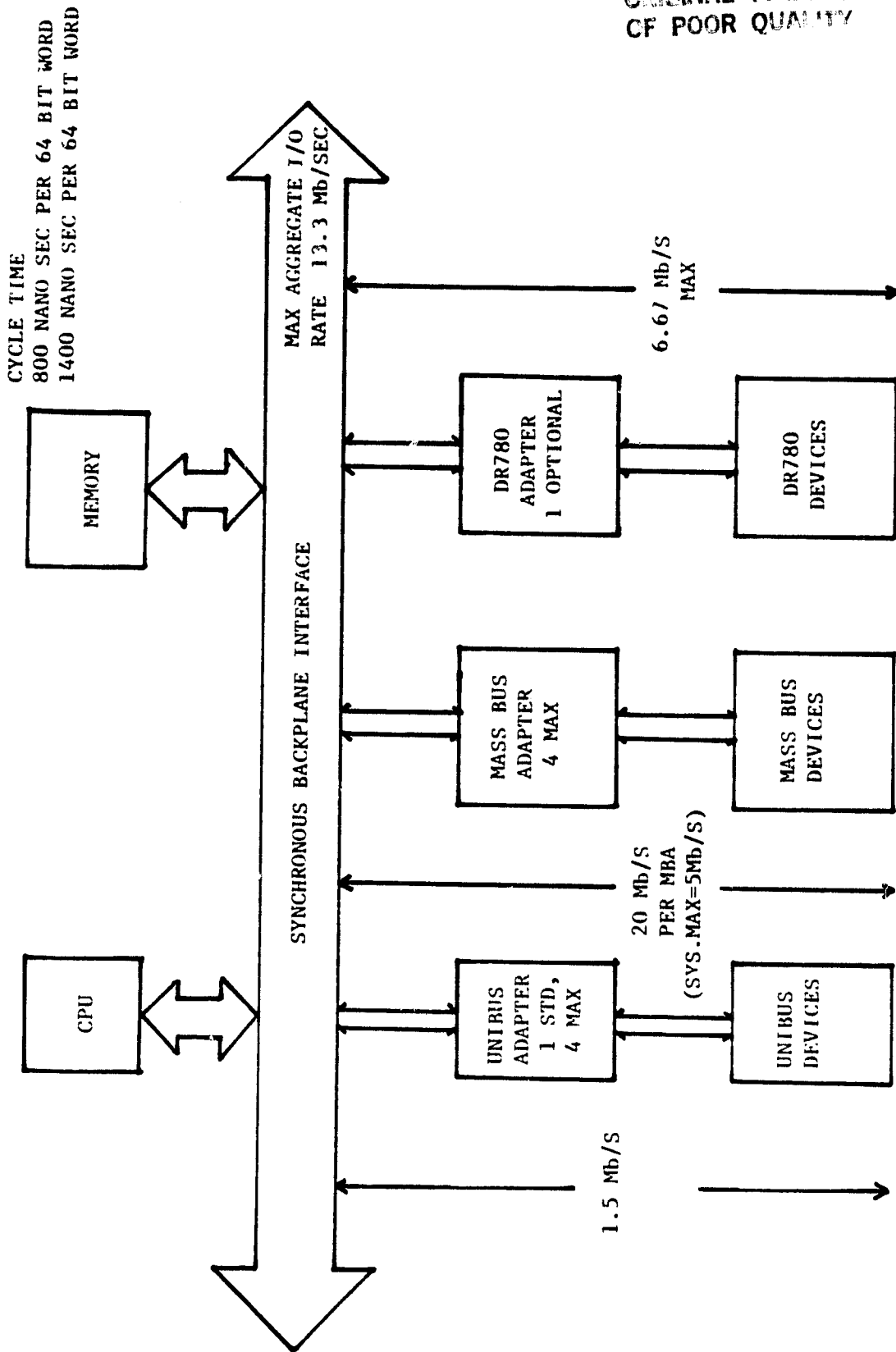


Figure 3. Block Diagram of VAX 11/780 Computer



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Figure 4. Basic Bus Configuration of VAX 11/780

The maximum, aggregate data transfer rate on the SBI is 13.3 megabytes per seconds which can be derived from the following information.

- . 200 Nanoseconds/cycle = 5 million cycles/second
- . Each cycle can carry an address (memory request) or for byte of data
- . Thus, one cycle is used to request eight bytes of data (to be read or written), and two cycles are used to carry data (at four bytes/cycle).
- . Five million cycles/second * 4 bytes/cycle = 20 million bytes/second
- . $20 * 2/3$ (1 of every 3 cycles is an address) = 13.3 million byte/second.⁴

The memory controller is the NEXUS used to interface the memory subsystem to the SBI. A system may have more than one memory controller as in the case of a two controller interleaved memory configuration.

The UNIBUS (UBUS) is a high speed asynchronous data system that allows communication between peripheral hardware and the VAX 11/780. The VAX 11/780 is capable of supporting 4 UBUS subsystems; one is standard with three more optional. The UBUS is connected to the SBI through a UBUS adapter (UBA) which performs priority arbitration among the devices on the UBUS. The primary functions of the UBA are to provide:

- (1) Access to UBUS address space from the SBI
- (2) Mapping of UBUS address to SBI addresses for UBUS direct memory access (DMA) transfers to system memory.
- (3) Data transfer paths for UNIBUS device access to random SBI memory addresses and high speed transfer for devices that transfer to consecutive increasing address.
- (4) UNIBUS interrupt fielding
- (5) UNIBUS priority arbitration.

All of these services are completely transparent to UBUS users.

The address mapping function is necessary because the UBUS has only 18 data lines thus providing an apparent memory addressing capability of 2^{18} or 200 kilobytes. The UBA, however, provides the capability of mapping the UNIBUS addresses into SBI addresses so that the full memory of the system can be accessed. (Full system memory is 16 array boards of 256 kilobytes each for a total of 4 megabytes.)

The UBA accepts either of two forms of input from the UBUS: hardware generated interrupt or direct memory access transfer. Each device connected to the UBUS uses one of five priority levels for requesting bus service. The NonProcessor Request (NPR) is used when the device requests a direct access transfer to memory or some other device and does not require processor intervention. A Bus Request (BR) is used when the device wishes to interrupt the BPU for service. Such service might be a CPU directed data transfer or the informing of some error condition that exists at the peripheral. The NPR has the highest priority with four levels of BR following (BR7-BR4).

Since there are only five priority levels and more than one device may be connected to a specific request level, if more than one device makes the same request, the device that is electrically closest to the UBS receives higher priority.

The Non Processor Request for direct memory access is a very important feature of the UBUS subsystem. These DMA transfers can be divided into two groups: random access of noncontiguous addresses and sequential access of sequentially increasing address. For random access, each UBUS transfer is made through the Direct Data Path (DDP, one per UNIBUS) and is mapped into an SBI transfer. This procedure allows only one word of data to be transferred during a single SBI cycle. For devices capable of requesting sequential access services, use is made of Buffered Data Path (BDP). Each UNIBUS provides 15 such BDPs. The BDP stores the data so that four UBUS transfers are performed for each SBI transfer.

The DDP must be used by devices not transferring to consecutive increasing addresses or by devices that mix read and write functions. The maximum throughput via the DDP is about 425 kilo words per second for write operations and 316 kilo words per second for each read operation. These rates will decrease as other SBI activity increases.⁴

Maximum published throughput via the BDP is about 695 kilo words per second for both read and write operations but actual expected throughput rates are only 1.5 mega bits per second. This rate will also decrease as other SBI activity increases.^{1,5} BDP transfers are

restricted to block transfers where a block is defined as equal to or greater than one byte. All transfers within the block must be to consecutive and increasing addresses and all transfers must be of the same function type (Read or Write).

The MASSBUS subsystem and the DR780 high performanc 32-bit parallel interface will not be described in this report since an understanding of their functional characteristics is not needed to determine their relative impacts on the HYPER channel network. The influence of both may be felt indirectly, however, since activity on the MASSBUS or DR780 will translate to SBI activity which will affect DDP and BDP transfer rates as described perviously.⁴

Likewise, the VAX CPU will not be described in detail but several comments may be made about the CPU's effects on throughput. The CPU represents the most intensive traffic load on the memory subsystem and hence on the SBI. Obviously if the processor is engaged in computing, it will request data much more often than it will write data. Fortunately the large memory cache (8 kilo bytes) available to the CPU reduces the SBI traffic load considerably.

In terms of the SBI traffic, impact on the processor's speed, published figures⁴ indicate that in a system with two memory controllers, the processor will be slowed about four percent per averaged megabyte per second of I/O traffic. The impact of a single memory controller is to slow the processor by a factor varying from two to four. Table 2 summarizes the DEC VAX 11/780 I/O characteristics.

TABLE 2. SUMMARY OF DEC VAX 11/780 DATA I/O CHARACTERISTICS

PROCESSOR : 32 bit words

MAIN MEMORY

Virtual Address Space : 4.3 billion bytes
 Cycle Time : 800 nanoseconds per 64-bit read
 1400 nanoseconds per 64-bit write

I/O UNIBUS Adapter

Maximum UNIBUS I/O Rate: 1.5 Mb/sec through buffered data paths.
 Buffered Data Path : 15 total, 8 byte buffer in each
 695 K words/second for read operations
 695 K words/second for write operations
 Used for fast DMA transfers
 *Direct Data Path : 425 K words/second for write operation
 316 K words/second for read operation
 Used for transfers to non-consecutive
 memory locations.

All data rates subject to degradation as traffic on SBI increases.
 (SBI allows communication interfaces between CPU, Memory, UNIBUS
 and MASSBUS.)

** Maximum aggregate throughputs on UNIBUS is only 1.5 Megabytes/
 second.

2.2.1.2 Perkin Elmer 3244

The Perkin Elmer (PE) 3240 series computer is a high throughput machine with a 32 bit architecture. The HOSC currently uses two PE 3244 machines with primary responsibilities as front end processors (FEP) receiving real time data streams from the KSC firing room.

A block diagram of the 3240 model computer is shown in Figure 5. Detailed information regarding the 3244 has not been obtained but a brief description of the 3244 architectures follows.

The 3244 memory subsystem is organized into banks each capable of handling 4 megabytes of addressable memory. Total system memory ranges from 256 kilo bytes in one bank to a full system complement of four 4 megabyte banks for a maximum of 16 megabytes of addressable memory. All memory is connected to a common memory bus which consists of two unidirectional, asynchronous, 32 bit busses. One bus is dedicated to memory write functions and the other is dedicated to memory read functions.

Input/Output is accomplished by up to five external communication busses: one multiplexer bus for medium speed devices and up to four high speed Direct Memory Access (DMA) busses. Each DMA bus supports eight high speed bidirectional ports. Each DMA port is controlled by a selector channel that controls and terminates transfers through the CPU. This selector channel is controlled through the multiplexer bus. Once the channel is activated, the processor is released and is free to continue processing. Published I/O transfer rates for the PE 3244 DMA bus indicate that transfer

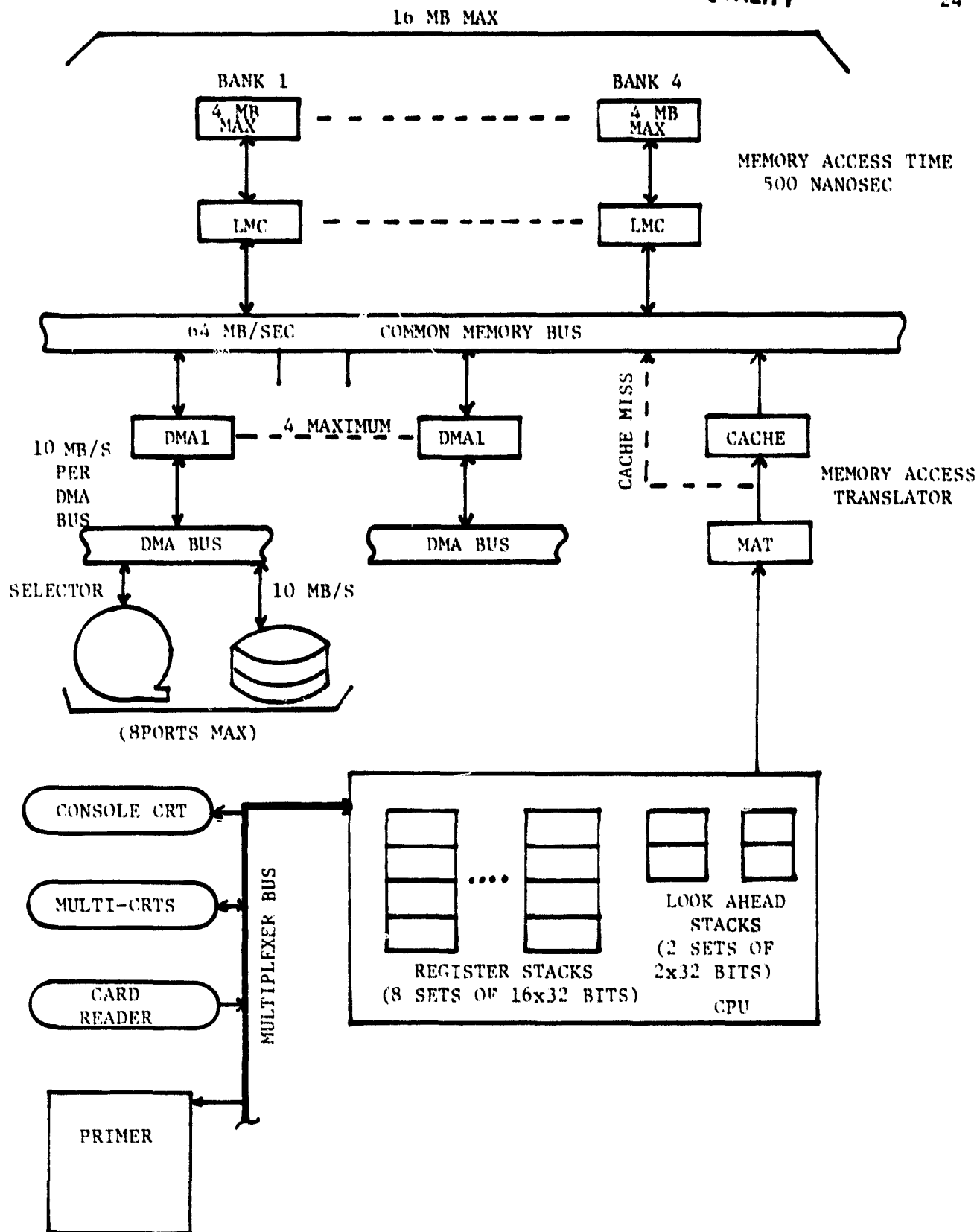


Figure 5. Block Diagram of PE 3244 Computer

rates of up to 10 megabytes per second burst mode are possible for each DMA bus.⁶ Table 3 summarizes the PE/3244 I/O characteristics.

2.2.1.3 Perkin Elmer 8/32c

Detailed information about the 8/32 computer has not been obtained, but conceptually, the 8/32 is a machine similar in architecture to the 3244. A significant difference is that the 8/32 is capable of supporting only one DMA bus. This DMA operates in a burst mode capable of transferring 6 megabytes per second. The 8/32 will allow configuration with a buffered selector channel that accomplish the 6 MB/s rate by transferring the data in 14 half-word blocks.⁷ Table 4 summarizes the estimated PE 8/32c I/O characteristics.

2.2.2 NSC HYPER channel

The Network System Corporation HYPER channel (HC) is a broadband local area communication network supporting data transmissions between network users at a rate of 50 megabyte per second. The HYPER channel network (HCN) serves to interface and interconnect various sizes of mainframe computers of differing manufacturers (e.g., UNIVAC, DEC, CRAY, PERKIN ELMER) with other peripherals such as data entry terminal card readers, printers, mass storage devices and other networks. Communication is provided over a passive 75 ohm coaxial cable called a trunk.

TABLE 3. SUMMARY OF PERKIN ELMER 3244 I/O CHARACTERISTICS

PROCESSOR : 32 bit/word

MAIN MEMORY

Virtual Address Space: 4 Megabytes
Basic Memory Access Time: 500 nanoseconds

DMA BUS DATA TRANSFER RATE: 10 Megabytes/second-burst mode
Maximum of 4 DMA busses can be supported.

TABLE 4. SUMMARY OF PERKIN ELMER 8/32 I/O CHARACTERISTICS

PROCESSOR : 32 bit/word

DMA BUS DATA TRANSFER RATE: 6 Megabytes/second by transferring
data in 14 half-word blocks.
Only one DMA can be supported.

Host computers gain access to the network trunk through hardware interfaces called processor adapters; unintelligent peripherals through device adapters. Network to network connection are accomplished with a link adapter which supports not only communication with standard transmission lines but also with microwave frequency RF links. Each network adapter may be connected to as many as four separate trunks and provides the service of trunk selection, trunk access, establishment of adapter to adapter virtual circuits and also provides user-to-adapter protocols. Network adapters contend for trunk control using a Carrier Sense Multiple Access scheme with prioritized staggered delays.⁹

The heart of the network is the A400 Adapter. The A400 is a microcomputer controlled interface device that allows up to 4 mini-computers of the same or mixed manufacturer types to transmit and receive data over the HYPER channel network. (All four trunk port may be connected to four channels of the same minicomputer.) The A400 provides a buffered interface between the trunk and the adapter. Some of this buffer is used to provide parallel to serial data stream conversion for host to trunk transmissions and serial to parallel conversion for trunk to host transmissions.

Each A400 adapter is composed of

- . a 16 bit microprocessor with 4906 words
of read only memory.
- . a storage section consisting of
 - 1024 8-bit bytes of control memory with
odd parity
 - 4096 8-bit bytes of control buffer with
odd parity

- 16 working registers
- 16 trunk registers
- 256 extension register
- . one trunk interface.

The adapter can be expanded to contain

- . 4 trunk interfaces
- . 8192 8-bit bytes of buffer memory
- . 1024 8 bit bytes of code conversion memory.

Additionally, the adapter has a peripheral device interface that provides a standard interface between the internal busses of the minicomputer and the A400. The peripheral interface adapter is separated from, but connected with ribbon cables to, the nucleus adapter which provides the hardware resources such as the microprocessor and memory register.^{8,10}
(See Figure 6)

To perform an operation on the network, the minicomputer loads the necessary parameters into the internal registers on the interface and requests the adapter to perform the indicated functions. Whenever an adapter is not performing a function, it scans all attached ports for a request to perform a function. When a function request is detected, the adapter suspends scanning and initiates the execution of the function. The flow diagram of Figure 7 illustrates the handshaking between the A400 and host processor when data transfers are initiated. Notice that the host processor initiates all actions of the adapter. (A compilation of functions that can be accomplished by the A400 is illustrated in Table 5.)⁸

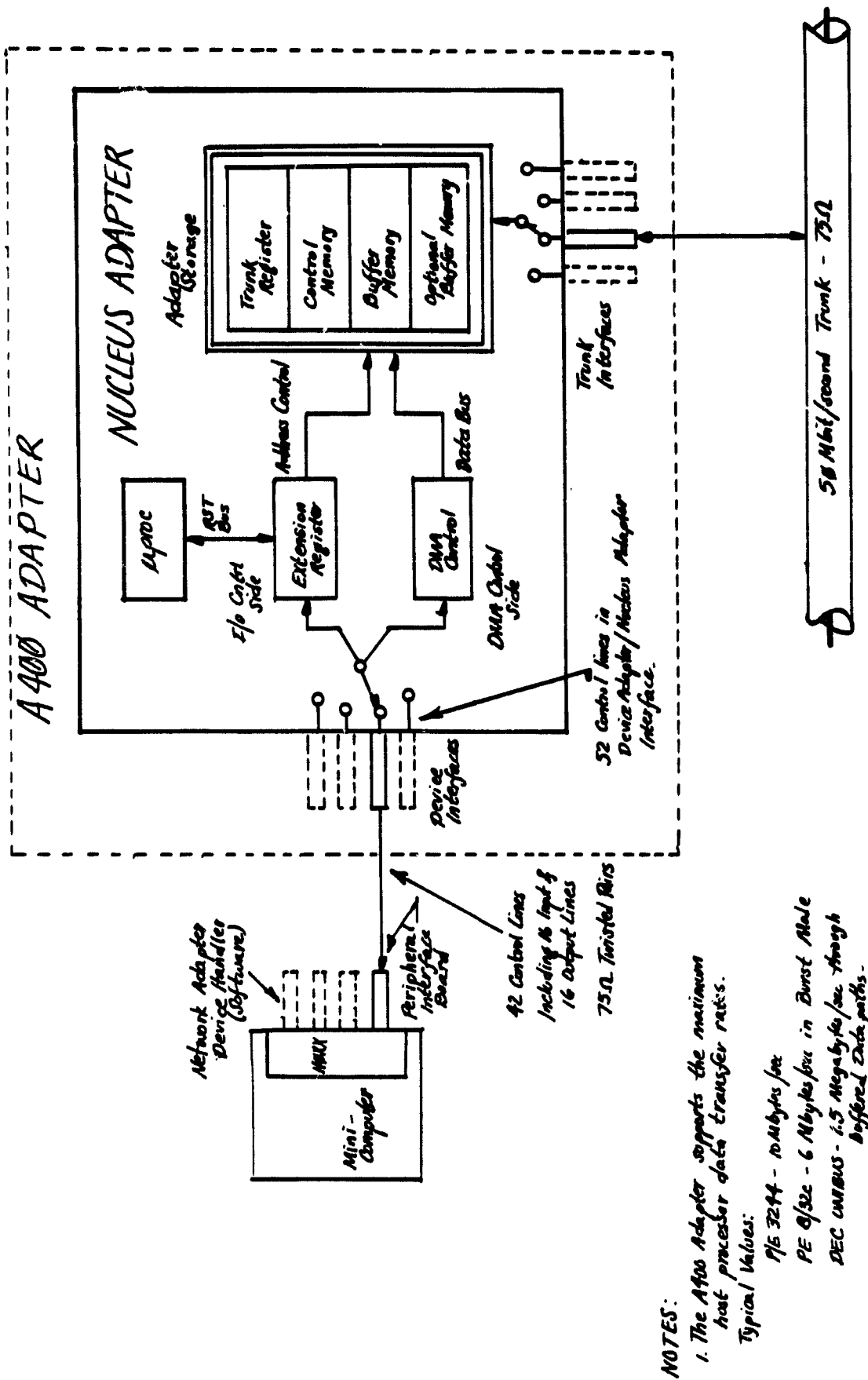


Figure 6. NSC HYPERchannel Adapter block diagram.

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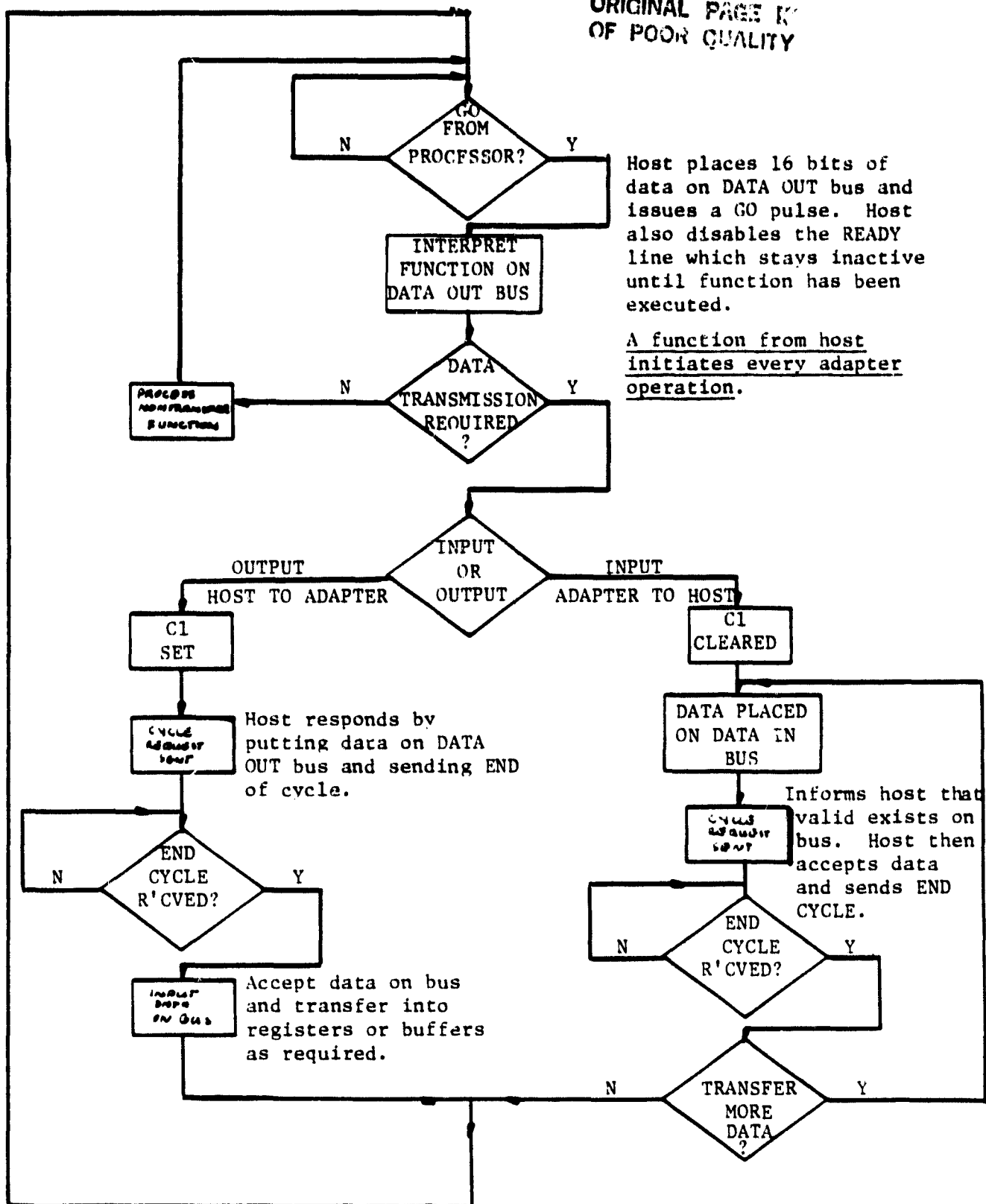


Figure 7. Host to A400 Adapter Data Exchange

TABLE 5. A400 ADAPTER FUNCTION DESCRIPTION

<u>CODE</u>	<u>FUNCTION</u>
04	Transmit message
08	Transmit data
0C	Transmit last data
10	Transmit local message
24	Input message
28	Input data
40	Status
50	Dump extension register
60	Mark down port 0
64	Mark down port 1
68	Mark down port 2
6C	Mark down port 3
70	Mark down port 0 and re-route messages
74	Mark down port 1 and re-route messages
78	Mark down port 2 and re-route messages
7C	Mark down port 3 and re-route messages
A0	Read statistics
A4	Read and clear statistics
C0	Set test
C4	Set address and length
C8	Write buffer
CC	Read buffer
E0	Clear adapter
E4	End operation
E6	Clear and wait for message
E8	Wait for message.

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Data can be transferred from host to adapter in two different modes: direct memory access (DMA) and register mode. In the DMA mode, the adapter uses an alternating buffer scheme. The adapter accepts data from the device into buffer memory. When the buffer is half full, trunk transmission of that amount of data is initiated as, the other half of the buffer is being filled. This filling and sending is continued until all data has been transferred. All DMA transfers are through the extension registers and are initiated by the adapter microprocessor and controlled by the adapter hardware.

In the register mode, data movement is also between the device interface and the nucleus adapter but the DMA controls are not used. These data transfers are also through the extension register but initiated and controlled by the microprocessor.

Data transfers from adapter to adapter are accomplished by the trunk interface. The trunk interface consists of a passive coaxial cable that transmits data serially between two adapters. Each trunk can have up to 64 drops depending on the length of the trunk cable and its transmission qualities.

Transmissions on a trunk are initiated and monitored by the trunk driver which is a microcode program stored in the adapter PROMs. The extension register and the trunk registers support the PROM trunk driver. When an adapter is ready to transmit, it must first contend for use of the trunk. The method for contention is called contention allocation. It is so called because the trunk is allocated to an adapter based on the adapter's need to transmit.

The contention process can be summarized as follows. The adapter first just attempts to transmit on the trunk. If the trunk is busy, the transmitter is disabled. When the trunk becomes free, a fixed delay is initiated by the adapter. This prevents the adapter from transmitting until the receiving party of the most recent transmission has had time to receive a response frame. Upon expiration of the fixed delay, another delay called the priority delay is initiated. This delay is different for each adapter and provides a unique time slot for each adapter on the trunk. Another delay, called end delay, is provided following the fixed delay. This delay is provided to insure that all adapters with higher priority have first access to the trunk. Obviously, with this trunk allocation scheme, higher priority adapters can dominate the trunk. To prevent this, each adapter has a flip flop in it that is known as the wait flip flop. This flip flop is set when the adapter transmits and is cleared when an end delay is signalled. This flip flop is intended to provide a more equitable contention environment. Although all adapters are equipped with wait flip flops, they may be disabled to provide assured trunk access.^{9,10} Figure 8 shows the flow of the wait algorithm.

Upon gaining access to the trunk, either a function message or data can be transmitted in trunk frames. When a frame is transmitted all adapters receive the frame. The adapter compares the received adapter access code which is part of the frame header with its own code which can be set by thumbwheel switches in the adapter. If and only if the codes match can the communication be accepted. (A zero in the receiving adapter code represents a "don't care" condition and

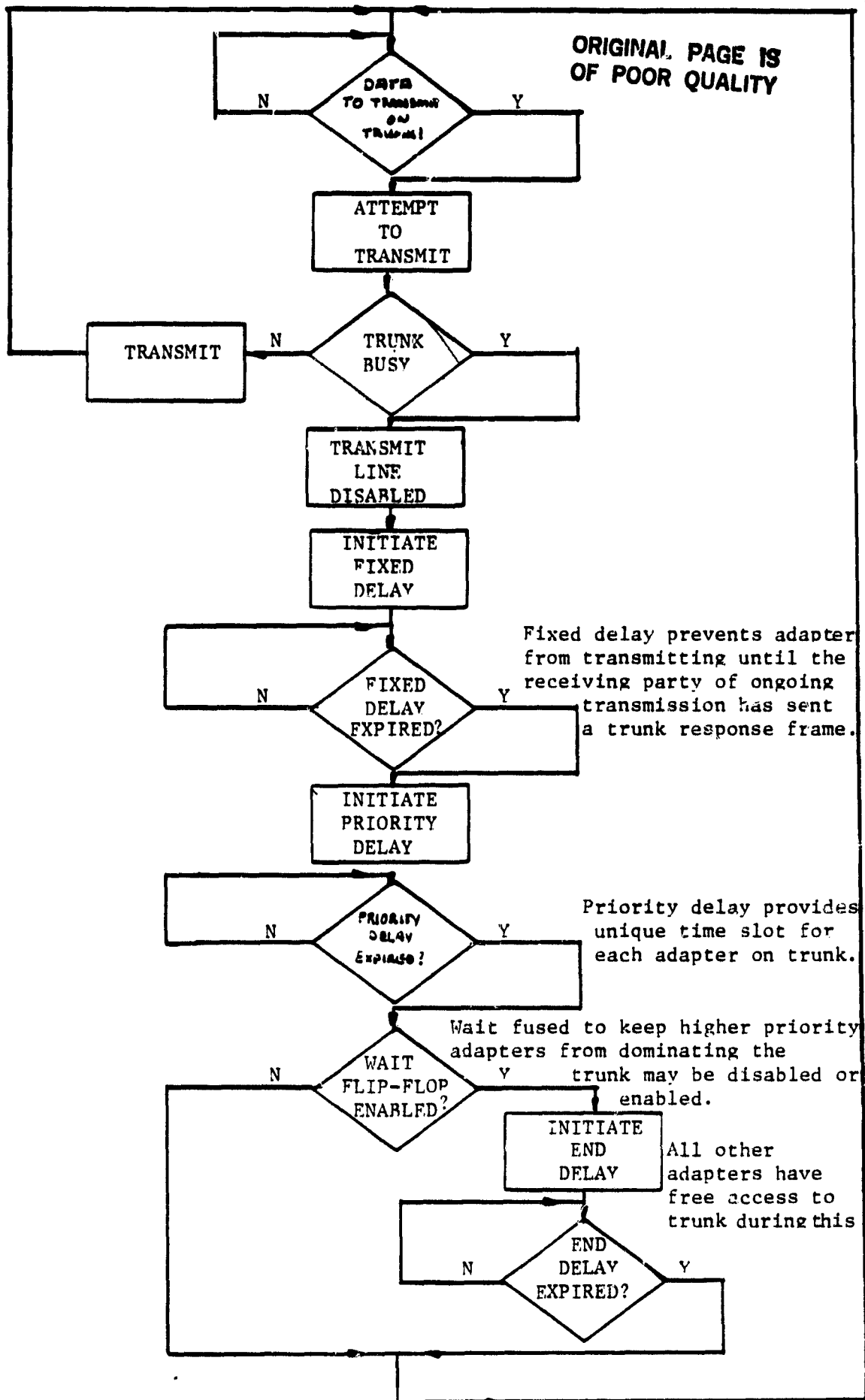


Figure 8. Trunk Contention Delay Algorithm

the receiving adapter will accept any character in that code position.)

The receiving adapter responds to the receipt of a trunk transmission with a trunk response frame. This notifies the sending adapter of the status of the received message. Every transmission frame requires the receipt of a response frame or the sending adapter will time out and retry the transmission. This process will be repeated 256 times. If unsuccessful at transmitting the message, the adapter will terminate the operation and record some status bits for the host in the adapter extension registers.¹⁰

2.2.3 Other HOSC Components

In addition to the computer resources are several other devices in the HOSC. Currently these other devices act as peripherals to the processors on the HYPER channel network and consequently do not directly affect traffic on the network. Indirectly, they represent overhead processor activity and thus slow traffic throughput on the processor I/O busses. These devices will include a Gandalf solid state switching matrix that acts to interface the MIPS consoles through VAX4. Also included in the peripherals are various strip recorders and three twelve-channel Genesco digital televisions that interface the engineering consoles through STS Primary (PE 3244), STS Backup (PE 8/32c) and Spacelab 8/32. No further descriptions of these devices are currently available.

3.0 HOSC ANALYSIS RESULTS

The HOSC system analysis initiated with a study of the system components, the computers, the HYPER channel network, the data flow activity of each device and the input-output characteristics of each device. The system operation is statistical in nature and, although a mathematical analysis is possible, it is not feasible to make such an analysis with much fidelity. Rather a simulation model that emulates the HOSC system with good fidelity can be used to achieve information concerning average bus traffic, average waiting time, collision frequency and maximum waiting times. Furthermore, these parameters can be investigated as a function of HOSC system configuration, input-output variations, and data file dump requirements.

The development of a simulation model with good fidelity has been accomplished. The HOSC system has been modeled with three different program simulations and these three algorithms have been compared against each other. The purpose in using three algorithms was to insure validity of the simulation results, a necessity due to the lack of sufficient system statistics to validate a single simulation algorithm. The three algorithms are similar, but have been programmed in BASIC, PASCAL and SLAM.

BASIC is an engineering oriented language not at all unlike FORTRAN. This simulation program is the main program. The program is listed in Appendix I.

Although many simulation runs were made with simple system configurations that allowed the simulation algorithm to be verified, there is no need to present those in this report. The monthly reports

document these earlier runs and the development of the algorithm. Rather it suffices to illustrate the simulation of the HOSC system as it is projected in configuration in Summer 1983.

3.1 Typical Basic Algorithm Information Printouts

Figure 9 depicts the HOSC simulation configuration which is documented in this report. This configuration is perhaps more complex than the actual system configuration for the present, but it is the type of configuration that is desired in the near future. Not all devices are transmitters of data in this system configuration. The A400 labeled port 4 only receives data transmitted on the HYPER channel bus. Other devices receive outside data and transmit and received data over the HYPER channel bus. This system configuration was devised at a meeting between this investigator and NASA/MSFC HOSC personnel on March 9, 1983, and is typical of the configurations to be utilized for HOSC applications in the near future.

In order to determine the number of simulation runs necessary to produce representative statistics and to let the system algorithm achieve steady state, as would occur in the actual system, several runs with the same system configuration parameters but with varying numbers of data transfers were made and the statistics compared.

Figures 10 and 11 illustrate the program printout that depicts the system configuration of figure 9. The # of bytes accumulated refers to the number of bytes which a particular device will accumulate refers to the number of bytes which a particular device will accumulate from a source before it transmits that data to the appropriate destination. As may be noted in Figure 10, there is a

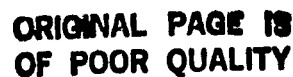


Figure 9. HOSC Simulation Configuration

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WAIT TIME OF PORT 1 BEFORE TRANSMISSION REQUEST (IN EVENT OF A COLLISION)
.000001
WAIT TIME OF PORT 2 BEFORE TRANSMISSION REQUEST (IN EVENT OF A COLLISION)
.000002
WAIT TIME OF PORT 3 BEFORE TRANSMISSION REQUEST (IN EVENT OF A COLLISION)
.000003
WAIT TIME OF PORT 4 BEFORE TRANSMISSION REQUEST (IN EVENT OF A COLLISION)
.000004

CHANNEL SETUP AND RELEASE TIME = .000025

CHANNEL DATA TRANSFER RATE = 6250000

PORT, DEVICE	TRANSFER RATE	LOAD TIMES
1 1	500000	.000002
2 1	500000	.000002
2 2	500000	.000002
3 1	500000	.000002
3 2	500000	.000002
3 3	3300000	3.0303030303E-7
4 1	3300000	3.0303030303E-7
4 2	1200000	8.3333333333E-7
4 3	1200000	8.3333333333E-7

COMBINATIONS (IJK - LMN)	PROBABILITY	TIME TO TRANSMIT Q(IJK) BYTES (IJK - LMN)
111311	.7	.004441
111321	.3	.004441
211431	1	.008345
221421	1	.013145
311321	.98	.001369
311221	.01	.001369
311211	.01	.001369
321311	.98	.001369
321221	.01	.001369
321211	.01	.001369
331421	1	8.6583333333E-4

Figure 11. System Configuration Parameters for 1333 Data Transfers.

data file dump of 64,000 bytes every 12 seconds as would be typical of a data file refresh operation.

The channel setup and release time is a parameter used to allow the HYPER channel to establish a transmission link and then to release the link after data transfer is complete.

As data is transferred across the system, each port vies for the bus in a contention scheme described in Section 2. Occasionally the ports will collide trying to transmit simultaneously. In Figures 12 and 13, a printout record of the results of a collision is illustrated. These printout records allow the operator to ensure the collision algorithm is working properly. As may be noted, 331 and 111 incurred a collision and 111 retransmitted first, since its assigned waiting time is less than 331.

Every time a data transfer occurs between two devices on the same port, the HYPER channel bus is not utilized and thus it is free for other transmissions except to the port involved in an inter-port data transfer. Figure 14 illustrates a printout record of an inter device data transfer. Figure 14 also illustrates a record of a data file dump.

The statistical printout of a simulation run is illustrated in Figure 15. This run had 1333 data transfers and over three million bytes transferred.

3.2 Results of A Simulation Run

Inspection of Figure 15 will illustrate the information garnered by a simulation run. The items of interest are the bus busy time, collision frequency of each source, the average waiting time, the longest waiting time and the relative activity of each source.

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A COLLISION OCCURRED BETWEEN 331 AND 111

ITERATION NUMBER 336

PROBABILITY .7
AUDOM NUMBER .064597057222
ROBABILITY 1

ESTIMATION 321

ORT, DEVICE, SOURCE	TOTAL TIME GONE BY SINCE LAST TRANSMISSION
1 1 1	.135719166711
1 1 1	.738061499984
1 1 1	.668460499377
1 1 1	.301452499979
1 1 1	.301452499979
1 1 1	1.000001
4 1 1	999999
4 1 1	999999
4 1 1	999999

ORT, DEVICE, SOURCE	REMAINING TIME TO TRANSMIT (NORMALIZED FASHION)
1 1 1	.000814166622
1 1 1	.201938506916
1 1 1	.331539500623
1 1 1	.210547500621
1 1 1	.210547500621
1 1 1	-.000001
4 1 1	999999
4 1 1	999999
4 1 1	999999

COLLISION NUMBER 3
DATA TRANSFER .004443

Figure 12. Typical Printout When Data Transfer Collision Occurs.

```

ORT, DEVICE, SOURCE      ACCUMULATED WAITING TIMES SINCE THE LAST TRANSMISSION:
1 1 1 0
2 1 1 .802502499984
3 1 1 .672901499977
4 1 1 .305893499979
5 1 1 .305893499979
6 1 1 1.004442
7 1 1 999999
8 1 1 999999
9 1 1 999999
DATA TRANSFER COMPLETED: TOTAL TIME GONE BY 28.340377331

ORT, DEVICE, SOURCE      REMAINING TIME TO TRANSMIT (NORMALIZED FASHION)
1 1 1 .136533333333
2 1 1 .197497500016
3 1 1 .327098500023
4 1 1 .206106500021
5 1 1 .206106500021
6 1 1 -.004442
7 1 1 999999
8 1 1 999999
9 1 1 999999
NEXT TRANSMISSION REQUEST BY 331
TOTAL TIME THAT WILL BE GONE BY WHEN 331 STARTS TO TRANSMIT 28.340377331

TERATION NUMBER 327

ROBABILITY 1
RANDOM NUMBER .608213251646

ESTIMATION 421

```

Figure 13. Typical Printout When Data Transfer Collision Occurs (Continued).

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TRANSMISSION ON SAME A400 SOURCE/DEST = 321311
NO COLLISION OCCURED

SOURCE 1, 1 DUMPED TO DESTINATION 4, 1 64000 BYTES OF DATA
TOTAL TIME GONE BY (BEFORE THE DUMP) 96.4481409909
TOTAL TIME GONE BY (AFTER THE DUMP) 96.5764859909

TRANSMISSION ON SAME A400 SOURCE/DEST = 311321
NO COLLISION OCCURED
TRANSMISSION ON SAME A400 SOURCE/DEST = 321311
NO COLLISION OCCURED

```

Figure 14. Typical Printout for Data File Dump and Inter Source Data Transfers.

* OF TIMES TRANSMITTED

111311	401
111321	195
111431	95
221421	34
311321	181
311321	3
311321	0
311321	185
311321	1
311321	1
311431	95
311431	1333
TOTAL	

PORT, DEVICE, SOURCE	LONGEST WAITING TIME	AVG WAITING TIME	COLLISIONS
1	-.128345	-5.71128979232E-2	1
1	-.128345	-.04111054163	1
2	-.045081	-1.71633610833E-2	2
2	-.128345	-5.57095512472E-2	0
2	-.129714	-6.26356917362E-3	0
3	-.10163916565	-.03009355127	2
4	0	0	0
4	0	0	0
4	0	0	0

TOTAL TIME GONE BY = 36.3994089909
 JS BUSY TIME = 6.1463251664

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Percent Bus Busy Time = 6.34%

PORT, DEVICE, SOURCE	AVG. WAIT TIME %	LONGEST WAIT %
1	27.18%	94.00%
2	4.11%	12.84%
2	1.72%	4.51%
3	10.88%	25.07%
3	1.22%	25.35%
3	3.01%	10.17%

Number of Data Transfer Collisions = 6 Collisions
 Number of Data File Dumps (11 to 41) = 8 (64,000 Bytes Each)
 Bytes Transferred by Data File Dump = 512,000 Bytes
 Bytes Transferred Source to Destination = 2,616,399 Bytes
 Total Bytes Transferred = 3,128,399 Bytes

Figure 15. Statistical Summary of 1333 Data Transfer Simulation.

For the 1333 data transfer simulation with data file dump of 64,000 bytes every 12 seconds the parameters of interest are depicted in the figure. Everything points to a satisfactory operation at this point with one notable exception, the longest waiting time. This Figure is -128.345 milliseconds (the negative sign indicates waiting time) for P.D.S. 111 which has an average time between transmission requests of 136.533 milliseconds (# of bytes accumulated divided by the average arrival rate of the bytes. The data is drawn from Figure 10.)

This waiting time amounts to 94% of the average time between transmission requests for P.D.S. 111 and serves a warning that P.D.S. 111 is on the verge of being overloaded. This may be alleviated by several means--changing the number of bytes to be accumulated before making a transmission request or by changing the data file dump time interval. In Section 3.4, this is discussed more fully.

3.3 Comparison of BASIC, PASCAL and SLAM Programs

Appendix II contains a listing of the PASCAL program and summary sheet for the HOSC simulation. The SLAM program listing is not included since NASA/MSFC does not carry SLAM software support. The three algorithms are compared in a broad sense in Table 6. The three algorithms were run for comparison purposes using 500 data transfers as the benchmark. The total bus time, bus utilization percentage, number of bytes transferred all compare very favorably. The number of collisions incurred vary due to differences in the collision algorithms used in the programs which were programmed by three different programmers as a check on the algorithms. The

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TABLE 6
COMPARISONS OF BASIC PASCAL AND SLAM PROGRAMS
NO DATA FILE DUMPS SIMULATED

	<u>BASIC</u>	<u>PASCAL</u>	<u>SLAM</u>
Total Transmissions	500	500	500
Elapsed Time on Bus (Seconds)	36.08	36.44	35.24
Collisions	8	29	19
Waits	N.T.	83	36
Bus Utilization	5.82%	4.69%	6.02%
Bytes Transferred	979,795	987,360	985,939

N.T. = Not Tabulated

The algorithms programmed all have the following features:

1. Allows up to 9 A400 adaptors
2. Allows up to 9 computer devices per A400 adaptor
3. Allows up to 9 data sources per device
4. Allows each device to transfer large block of data on a periodic basis such as for CRT data base refresh
5. Allows assignment of individual waiting times to be assigned to each A400 in event of a collision
6. Allows the shortest assigned waiting time A400 to retry a transmission in the event of a collision
7. If a data transfer occurs between two devices on the same A400 (Inter A400 data transfer) it allows this to occur without tying up the bus
8. Allows for individual source data arrival rates
9. Allows for individual source data buffer sizes (relates to time between transmission requests)
10. Allows for individual device to A400 I/O data rates.

The results indicate no major discrepancies lie in the HOSC system model used for the algorithm development. The BASIC program has been emphasized since it is more transportable than SLAM or PASCAL. However, for a next generation simulation model, PASCAL will be constructed in a user friendly format since it has some features which make it suitable for this type of simulation.

3.4 Conclusions

Results of a fair run simulation using the configuration of Figure 9 has been tabulated in Table 7. The purpose of this comparison was to determine:

TABLE 7

COMPARISON OF FOUR SIMULATION RUNS
(BASIC PROGRAM)

P.D.S.	TTMR(MS)	LONGEST WAITING TIME (MS, % TTMR)			RUN E	AVERAGE WAITING TIME (Z)				BUS BUSY TIME (%)			
		RUN A	RUN B	RUN C	RUN D	RUN A	RUN B	RUN C	RUN D	RUN A	RUN B	RUN C	RUN D
1 1 1	136	128 (94%)	128 (94%)	256 (188%)	128 (94%)	8 (6%)	29	27	45	29	39		
2 1 1	1000	128 (13%)	128 (13%)	118 (11.8%)	19 (2%)	4 (.4%)	2.8	4.1	4.8	1.7	.22	6.75%	6.34%
2 2 1	1000	45 (5%)	45 (5%)	8.4 (.84%)	8 (.8%)	9 (1%)	1.5	1.7	.35	.47	.42		
3 1 1	512	129 (25%)	128 (25%)	256 (50%)	116 (23%)	5 (1%)	1.9	10.9	11.5	1.6	.21		
3 2 1	512	128 (25%)	130 (26%)	258 (50%)	116 (23%)	4 (1%)	3.2	1.2	.85	1.2	.57		
3 3 1	1000	101 (10%)	101 (10%)	292 (29%)	101 (10%)	13 (1.3%)	3.0	3.0	2.8	3.2	.81		

P.D.S - PORT, DEVICE, SOURCE

Average Waiting Time is expressed as percent of TTMR

TTMR = Time to Next Transmission Request = Time Between Data Transfer Requests

Number of Collisions: RUN A - 10; RUN B - 6; RUN C - 14; RUN D - 3; RUN E - 3

RUN A 1500 data transfers, Data file dump once every 12 seconds, 64,000 Bytes each dump
 RUN B 1333 data transfers, Data file dump once every 12 seconds, 64,000 Bytes each dump
 RUN C 1000 data transfers, Data file dump once every 12 seconds, 128,000 Bytes each dump
 RUN D 500 data transfers, Data file dump once every 12 seconds, 64,000 Bytes each dump
 RUN E 500 data transfers, Data file dump once every 120 seconds, 64,000 Bytes each dump
 (For a 500 iteration run time of 36 milliseconds no data FILE dump occurs).

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- a) The number of iterations necessary to produce consistent results.
- b) Determine the effects of varying the data file dump interval.

Since each simulation run uses random number generators as part of the program, it should not be surprising to see small differences in the output summary statistics. Indeed two runs of the same configuration with the same number of iterations will produce slightly different results. This is completely expected and does not reduce the value of the results at all.

The results tabulated in Table 7 indicate that the system performs as desired with one exception. The data file dump. Every 24 seconds, Run C, creates a waiting time of 256 milliseconds for P.D.S. 111, which is 188% of the time between transmission requests for source P.D.S. 111. The impact of these results from a realization that 100% waiting time for a source would mean that source has been waiting for an opportunity to transmit for such a long time that it now has three transmission requests rather than one. It is obvious from the data in Table 7 that large data file dumps will tend to create a log jam for devices with active external data sources. These external data sources may not be extinguishable; hence, the need to provide sufficient data storage in the device to hold incoming data during a large data file dump is paramount.

An analytical feeling for this problem is easily derived. Any source with a data file dump will experience an external source transmission request whenever the data file dump time exceeds the total time between transmission requests for that source. In the

configuration of Figure 9, we have a data file dump from P.D. 11 to P.D. 41. The I/O rate for P.D. 11 is 500 K Bytes per second and the I/O rate for P.D. 41 is 3.30 M Bytes per second. The channel transfer rate is 6.25 M Bytes per second so that data file dump will take a total time equal to the channel setup release time plus the time to dump X Bytes at the slowest I/O rate or for our configurations the data file dump time (DFDT) is

$$DFDT = 25 \text{ msec} + X(2 \text{ msec/Byte}) .$$

For 64,000 Bytes DFDT = 128 msec and

for 128,000 Bytes DFDT = 256 msec.

In fact whenever the DFDT exceeds the fastest source average time to transmission request time a problem will definitely arise. For the example where the number of bytes in a data file dump exceeds

$$\begin{array}{l} \text{Number of Bytes} \\ \text{In Data File Dump} \end{array} = \frac{126.533 - .025}{2 \times 10^{-3}} = 68254 \text{ Bytes.}$$

the fastest source will possibly incur two transmission requests.

There are several ways to correct this situation:

- a) Increase the buffer size of the source so it can store more than two full sets of data between transmission opportunities. This is a viable option since source P.D.S. 111 is the most active with highest outside data arrival rate and only 2048 bytes to be accumulated between transmission requests.

- b) Perform data file dumps more often but transfer proportionally less bytes per dump thereby reducing any sources waiting time. This may not be a viable option due to the lack of data file data being ready in a somewhat steady occurrence rate. Also this would require a data file storage medium at the destinations; however, this is usually the case.
- c) Break up the data file dump by transmitting it in smaller packets. That is rather than 64,000 bytes in a steady stream for a short time once every 12 seconds, transmit 8,000 bytes, break and release channel to allow another user but request transmission rights immediately and repeat for 8 times. This would transfer the data in almost the same time as sending all 64,000 bytes while allowing the active devices a chance to clear their stored data.

All the above options could be accomplished through software programming of the source device P.D. 11; thus, it is a HOSC system operators choice of which method to utilize.

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APPENDIX I

BASIC SIMULATION ALGORITHM PROGRAM

A listing of the BASIC simulation algorithm program is presented in this appendix. The program contains plentiful comments and is user oriented, with prompts and options displayed on the interactive screen. The BASIC language is common to many machines; however, the input output commands are usually particular to a single machine, in this case the HP-87 system.

```

350 DISP "3 OF SOURCES FOR PORT ":I:" DEVICE " :K: INPUT SRC(I,N)
360 NEXT I * I=I+1 * IF I<=HY THEN 340 ELSE I=0

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740 IF LA8(J,K)=0 THEN ATR8(J,K)=999999 ELSE ATR8(J,K)=Q8(J,K)/LA8(J,K)
750 NEXT K
760 LA=LA8(J,K)
770 FOR J=1 TO DV(I)
780   Q=Q8(J,K)
790   FOR K=1 TO SR(I,J)
800     Q=Q8(J,K)
810     NEXT K
820     NEXT J
830     IF LA9(J,K)=0 THEN ATR9(J,K)=999999 ELSE ATR9(J,K)=Q9(J,K)/LA9(J,K)
840     NEXT K
850     NEXT J
860     LA=LA9(J,K)
870     FOR I=1 TO HY
880       Q=Q9(J,K)
890       NEXT I
900       CLEAR
910       DISP "CHANNEL SETUP AND RELEASE TIME ASSIGNED THE VALUE 25 MICROSECO
DS."
920   Q$="000025"
930   DISP " "
940   DISP "PRESS <END LINE> OR ENTER THE PROPER VALUE "
950   INPUT Q$
960   IF Q$ <> "" THEN TSR=VAL(Q$) ELSE TSR=.000025
970   DISP " "
980   DISP "CHANNEL DATA TRANSFER RATE IS 6.25MBS"
990   Q$="6250000"
1000  DISP "PRESS <END LINE> OR ENTER THE PROPER VALUE "
1010  INPUT Q$
1020  IF Q$ <> "" THEN CR=VAL(Q$) ELSE CR=6250000
1030  CLEAR
1040  FOR I=1 TO HY
1050    FOR J=1 TO DV(I)
1060      DISP "RATE AT WHICH DATA IS TRANSFERRED FROM DEVICE ";J;" TO PORT ";I:
1070      INPUT R(I,J)
1080    NEXT J
1090  NEXT I
1100  LI(I,J)=1/R(I,J)
1110  NEXT J
1120  GOSUB 960
1130  FOR I=1 TO HY
1140    FOR J=1 TO DV(I)
1150      FOR K=1 TO SR(I,J)
1160        MNEG1(J,K)=0
1170        MNEG2(J,K)=0
1180        MNEG3(J,K)=0
1190        MNEG4(J,K)=0
1200        MNEG5(J,K)=0
1210        MNEG6(J,K)=0
1220        MNEG7(J,K)=0
1230        MNEG8(J,K)=0
1240        MNEG9(J,K)=0
1250        NUMPROB=0
1260        PDS(0)=0
1270        PROB(0)=0
1280        FOR I=1 TO 200
1290          CLEAR
1300          DISP "PREVIOUS COMBINATION "
1310          PROB(I-1)=PROB(I-1)
1320          DISP "SELECT (PORT,DEVICE,SOURCE) WHICH TRANSMITS TO (PORT,DEVICE,SOURCE) "

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1110 DISP "FORMAT <PDS>". THESE COMBINATIONS MUST BE ENTERED IN ORDER. "
1120 DISP "ENTER <0> IF FINISHED ENTERING PROBABILITIES "; INPUT PDS(I)
1130 I=0
30 IF PDS(I)=0 THEN 1260 ELSE NUMPROB=NUMPROB+1
40 DISP " " & DISP "PROBABILITY OF OCCURRENCE "; INPUT PRO(I)
50 GOSUB 1160 GOTO 1250
50 J=INT (PDS(I)/100000) & K=INT (PDS(I)/10000)-J*10
70 L=-(J*100)-K*10+INT (PDS(I)/1000) & M=-(J*1000)-K*100-L*10+INT (PDS(I)/100)

30 N=-(J*10000)-K*100-L*100-M*10+INT (PDS(I)/10)
30 O=-(J*100000)-K*10000-L*1000-M*100-N*10+PDS(I)
30 A=J & K=L & ON A GOSUB 480,520,560,600,640,680,720,760,800
10 CP1=QQ*LI(A,J)
20 CP2=QQ*LI(M,N)
30 IF CP1>CP2 THEN IL=CP1 ELSE IL=CP2
40 I(I)=ISR+2000/CR+IL & RETURN
50 NEXT I & PREJ=99 & PREK=95
60 SNUM=0 & PREJ=99 & PREK=99 & CLEAR & FOR I=1 TO 200 & CLEAR & DISP "ENTER I
IJ-LM COMBINATIONS WHICH HAVE SOURCE DATA FILE DUMPS."
70 DISP "ENTER A <0> WHEN COMPLETED." & DISP " " & DISP "SOURCE TO DESTINATION
" & INPUT DIFL(I) & IF DIFL(I)=0 THEN 1330
80 WT(I)=0 & SNUM=SNUM+1 & J=INT (DIFL(I)/1000) & K=DIFL(I)-J*1000 & K=INT (K/
10)
90 IF J=PREJ AND K=PREK THEN BYTES(I)=BYTES(I-1) & TIM(I)=TIM(I-1) & GOTO 1320

00 PREJ=J & PREK=K & DISP " " OF BYTES TO BE DUMPED " & INPUT BYTES(I)
10 DISP "TIME INTERVAL BETWEEN DUMPS (IN SECONDS)"; INPUT TIM(I)
20 NEXT I
30 IF AN=2 THEN 1720
34 ! *****
35 ! READ OR WRITE DATA TO THE DATA FILE (GEIDATA = 999 -READ OR 888 -WRITE)
36 ! LINES 1340-1710
37 ! *****
40 CLEAR & CREATE DFNAME$,2123.8
50 DFNAME$=DFNAME$":D700" & ASSIGN# 1 TO DFNAME$
60 IF GEIDATA=999 THEN READ# 1,1 : HY ELSE PRINT# 1,1 : HY
70 II=1 & FOR J=1 TO HY & II=II+1
80 IF GEIDATA=999 THEN READ# 1,II : DV(J) ELSE PRINT# 1,II : DV(J)
90 NEXT J & FOR I=1 TO HY & FOR J=1 TO DV(I)
00 II=II+1 & IF GEIDATA=999 THEN READ# 1,II : SR(I,J) ELSE PRINT# 1,II : SR(I,
1410 NEXT J & NEXT I
1420 FOR I=1 TO HY & FOR J=1 TO DV(I) & FOR K=1 TO SR(I,J)
1430 IF GEIDATA=888 THEN ON I GOSUB 480,520,560,600,640,680,720,760,800

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1440 II=II+1
1450 IF GETDATA=999 THEN READ# 1,II : QQ(J,K) ELSE PRINT# 1,II : GO
1460 II=II+1
1470 IF GETDATA=999 THEN READ# 1,II : LA(J,K) ELSE PRINT# 1,II : LA
1480 NEXT K & NEXT J & IF GETDATA=999 THEN ON I GOSUB 450,490,530,570,610,650,69
,730,770
490 NEXT I
500 FOR I=1 TO HY & FOR J=1 TO DV(I) & II=II+1
510 IF GETDATA=999 THEN READ# 1,II : D(I,J) ELSE PRINT# 1,II : D(I,J)
520 NEXT J & NEXT I
530 FOR I=1 TO HY & II=II+1 & IF GETDATA=999 THEN READ# 1,II : W(I) ELSE PRINT#
1,II : W(I)
540 NEXT I
550 II=II+1 & IF GETDATA=999 THEN READ# 1,II : TSR ELSE PRINT# 1,II : TSR
560 II=II+1 & IF GETDATA=999 THEN READ# 1,II : CR ELSE PRINT# 1,II : CR
570 FOR I=1 TO HY & FOR J=1 TO DV(I) & II=II+1
580 IF GETDATA=999 THEN READ# 1,II : R(I,J) & L(I,J)=1/R(I,J) ELSE PRINT# 1,II
R(I,J)
590 NEXT J & NEXT I
600 II=II+1 & IF GETDATA=999 THEN READ# 1,II : NUMPROB ELSE PRINT# 1,II : NUMPR
610 FOR I=1 TO NUMPROB & II=II+1 & IF GETDATA=999 THEN READ# 1,II : PDS(I) ELSE
PRINT# 1,II : PDS(I)
620 II=II+1 & IF GETDATA=999 THEN READ# 1,II : PRO(I) ELSE PRINT# 1,II : PRO(I)
630 IF GETDATA=999 THEN TIMEST(I)=0 & GOSUB 1160
640 NEXT I
650 II=II+1 & IF GETDATA=999 THEN READ# 1,II : SUM ELSE PRINT# 1,II : SUM
660 FOR I=1 TO SUM & WT(I)=0 & II=II+1 & IF GETDATA=999 THEN READ# 1,II : DIFL
I) ELSE PRINT# 1,II : DIFL(I)
670 II=II+1 & IF GETDATA=999 THEN READ# 1,II : BYTES(I) ELSE PRINT# 1,II : BYTE
(I)
680 II=II+1 & IF GETDATA=999 THEN READ# 1,II : TIM(I) ELSE PRINT# 1,II : TIM(I)
690 NEXT I
700 ASSIGN# 1 TO *
710 GOSUB 960
720 CLEAR & DISP TAB (20):"MENU" & DISP " " & DISP "1 -- PRINT OUT CURRENT DA
A" & DISP "2 -- CORRECT DATA ERRORS"
730 DISP "3 -- RUN PROGRAM " & DISP " " & DISP "ENTER YOUR SELECTION " :& INPU
, SELECT
1740 IF SELECT<1 OR SELECT>3 THEN 1720
1750 IF SELECT=3 THEN 1760 ELSE ON SELECT GOTO 3840,4210
1760 SAVEAIR=99999 & SAVELA=0
1770 FOR I=1 TO HY & FOR J=1 TO DV(I) & FOR K=1 TO SR(I,J)
1780 ON I GOSUB 480,520,560,600,640,680,720,760,800

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1790 IF LA>SAVELA THEN SAVELA=LA & SAVEIJK=I*100+J*10+K
1800 IF ATR=0 THEN 1820
1810 IF ATR<SAVEATR THEN SAVEATR=ATR & SAVEATRIJK=I*100+J*10+K
1820 IF ATR=999999 THEN TLST=999999 ELSE TLST=0
1830 ON I GOSUB 1850,1860,1870,1880,1890,1900,1910,1920,1930
1840 NEXT K & NEXT J & NEXT I & GOTO 1940
1850 TLST1(J,K)=TLST & RETURN
1860 TLST2(J,K)=TLST & RETURN
1870 TLST3(J,K)=TLST & RETURN
1880 TLST4(J,K)=TLST & RETURN
1890 TLST5(J,K)=TLST & RETURN
1900 TLST6(J,K)=TLST & RETURN
1910 TLST7(J,K)=TLST & RETURN
1920 TLST8(J,K)=TLST & RETURN
1930 TLST9(J,K)=TLST & RETURN
1940 PRINT " " & PRINT "FIRST USER IS ";SAVEIJK & ITERNUM=0 & NUMDEST=0
1950 PRINT " " & PRINT "TOTAL TIME GONE BY ";SAVEATR & CN=0 & TTGB=SAVEATR
1960 CLEAR & DISP " " & PRINT "PRINTER MENU" & DISP " " & DISP "1" -- NO PRIN
1970 GIVEN ON EACH CYCLE"
1980 DISP "2" -- PRINTOUT GIVEN ON EACH CYCLE" & DISP "3" -- SPECIFY THE LAST
1990 CYCLES TO BE PRINTED" & DISP " "
2000 INPUT PR & IF PR<1 OR PR>3 THEN CLEAR & GOTO 1960
2010 IF PR=2 THEN PRINTER IS 701 ELSE PRINTER IS 1
2020 IF PR=3 THEN DISP "ENTER THE LAST X CYCLES WHICH ARE TO BE PRINTED "; & INPUT
NUMCYCLES
2030 ! *****
2040 ! GENERATE A RANDOM NUMBER. THEN USE IT TO SELECT THE DESIGNATION DEVICE
2050 ! LINES 2010-2140
2060 ! *****
2070 ! XXX=RND
2080 IF PR=3 THEN 2030 ELSE 2040
2090 IF ITERNUM=NUMITER-NUMCYCLES THEN PRINTER IS 701
2100 PRINT " " & PRINT " " & PRINT "ITERATION NUMBER "; ITERNUM+1 & PRINT " "
2110 FOR I=1 TO NUMPROB
2120 IJK=INT (PDS(I)/1000)
2130 IF IJK=SAVEIJK THEN NUMDEST=NUMDEST+1 & SAVEEND=I ELSE 2090
2140 IF NUMDEST=1 THEN START=I & PROB=PROB(I) & PRINT "PROBABILITY "; PROB
2150 NEXT I & PRINT "RANDOM NUMBER "; XXX
2160 IF XXX<PROB THEN SOURCE=START & GOTO 2140
2170 FOR I=START TO SAVEEND
2180 IF XXX<= PROB THEN SOURCE=I & GOTO 2140
2190 PROB=PROB+PROB(I+1) & PRINT "PROBABILITY "; PROB & NEXT I
2200 PRINT " " & PRINT "DESTINATION "; PDS(SOURCE)-SAVEIJK*1000
2210 PRINT " " & PRINT "PORT, DEVICE, SOURCE
TRANSMISSION" & TTGBSLI=SAVEATR
TOTAL TIME GONE BY SINCE LAST

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2160 TIMESTT(SOURCE)=TIMESTT(SOURCE)+1
2170 FOR I=1 TO HY @ FOR J=1 TO DV(I) @ FOR K=1 TO SR(I,J)
2180 TLST=999999 @ ON I GOSUB 2210,2220,2230,2240,2250,2260,2270,2280,2290
2190 PRINT I;" ";J;" ";K;TAB(30);TLST
2200 NEXT K @ NEXT J @ NEXT I @ GOTO 2300
2210 IF TLST1(J,K)=999999 THEN RETURN ELSE TLST1(J,K)=TLST1(J,K)+ITGBSLI @ TLST1=
    ST1(J,K) @ RETURN
2220 IF TLST2(J,K)=999999 THEN RETURN ELSE TLST2(J,K)=TLST2(J,K)+ITGBSLI @ TLST1=
    ST2(J,K) @ RETURN
2230 IF TLST3(J,K)=999999 THEN RETURN ELSE TLST3(J,K)=TLST3(J,K)+ITGBSLI @ TLST1=
    ST3(J,K) @ RETURN
2240 IF TLST4(J,K)=999999 THEN RETURN ELSE TLST4(J,K)=TLST4(J,K)+ITGBSLI @ TLST1=
    ST4(J,K) @ RETURN
2250 IF TLST5(J,K)=999999 THEN RETURN ELSE TLST5(J,K)=TLST5(J,K)+ITGBSLI @ TLST1=
    ST5(J,K) @ RETURN
2260 IF TLST6(J,K)=999999 THEN RETURN ELSE TLST6(J,K)=TLST6(J,K)+ITGBSLI @ TLST1=
    ST6(J,K) @ RETURN
2270 IF TLST7(J,K)=999999 THEN RETURN ELSE TLST7(J,K)=TLST7(J,K)+ITGBSLI @ TLST1=
    ST7(J,K) @ RETURN
2280 IF TLST8(J,K)=999999 THEN RETURN ELSE TLST8(J,K)=TLST8(J,K)+ITGBSLI @ TLST1=
    ST8(J,K) @ RETURN
2290 IF TLST9(J,K)=999999 THEN RETURN ELSE TLST9(J,K)=TLST9(J,K)+ITGBSLI @ TLST1=
    ST9(J,K) @ RETURN
2300 GOSUB 2310 @ GOTO 3020
305 ! *****
306 ! FIND SMALL & SMALL2 TO BE USED TO CHECK FOR COLLISIONS AND NR11
307 ! LINES 2310-2540
308 ! *****
310 PRINT " " @ PRINT "PORT,DEVICE,SOURCE" REMAINING TIME TO TRANSMIT (NO
    ALIZED FASHION)
320 SMALL=999988 @ SAVESM=999988 @ SMALL2=99988 @ SAVESM2=99988
330 FOR I=1 TO HY @ FOR J=1 TO DV(I) @ FOR K=1 TO SR(I,J)
340 ON I GOSUB 480,520,560,600,640,680,720,760,800
350 ON I GOSUB 2370,2380,2390,2400,2410,2420,2430,2440,2450
360 GOTO 2460
370 TLST=TLST1(J,K) @ RETURN
380 TLST=TLST2(J,K) @ RETURN
390 TLST=TLST3(J,K) @ RETURN
400 TLST=TLST4(J,K) @ RETURN
410 TLST=TLST5(J,K) @ RETURN
420 TLST=TLST6(J,K) @ RETURN
430 TLST=TLST7(J,K) @ RETURN
440 TLST=TLST8(J,K) @ RETURN
2450 TLST=TLST9(J,K) @ RETURN

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2460 IF ATR=999999 THEN NRTT=999999 ELSE NRTT=ATR-1LST
2470 IF CLSN$="Y" THEN SUB=-1 ELSE SUB=1
2480 ON I GOSUB 2550,2600,2650,2700,2750,2800,2850,2900,2950
2490 IF CLSN$="Y" THEN 2510
2500 PRINT I;" ";J;" ";K;TAB (30);NRTT
2510 IF NRTT<SMALL THEN CH1=SMALL @ SMALL=NRTT @ CH2=SAVESM @ SAVESH=I*100+J*10+
      @ EQ$="Y"
2520 IF CH1<SMALL2 THEN SMALL2=CH1 @ SAVESM2=CH2
2530 IF NRTT=SMALL AND NRTT<SMALL2 AND EQ$="N" THEN SMALL2=NRTT @ SAVESM2=I*100+
      @10+K
2540 EQ$="N" @ NEXT K @ NEXT J @ NEXT I @ EQ$="N" @ RETURN
2550 NRTT1(J,K)=NRTT @ IF NRTT<0 THEN ING1(J,K)=ING1(J,K)+NRTT @ IN1(J,K)=IN1(J,
      )+SUB
2560 IF CLSN$ <> "Y" THEN 2580
2570 IF NRTT<0 THEN ING1(J,K)=ING1(J,K)-2*NRTT
2580 IF NRTT<MNEG1(J,K) THEN MNEG1(J,K)=NRTT
2590 RETURN
2600 NRTT2(J,K)=NRTT @ IF NRTT<0 THEN ING2(J,K)=ING2(J,K)+NRTT @ IN2(J,K)=IN2(J,
      )+SUB
2610 IF NRTT<MNEG2(J,K) THEN MNEG2(J,K)=NRTT
2620 IF CLSN$ <> "Y" THEN 2640
2630 IF NRTT<0 THEN ING2(J,K)=ING2(J,K)-2*NRTT
2640 RETURN
2650 NRTT3(J,K)=NRTT @ IF NRTT<0 THEN ING3(J,K)=ING3(J,K)+NRTT @ IN3(J,K)=IN3(J,
      )+SUB
2660 IF CLSN$ <> "Y" THEN 2680
2670 IF NRTT<0 THEN ING3(J,K)=ING3(J,K)-2*NRTT
2680 IF NRTT<MNEG3(J,K) THEN MNEG3(J,K)=NRTT
2690 RETURN
2700 NRTT4(J,K)=NRTT @ IF NRTT<0 THEN ING4(J,K)=ING4(J,K)+NRTT @ IN4(J,K)=IN4(J,
      )+SUB
2710 IF CLSN$ <> "Y" THEN 2730
2720 IF NRTT<0 THEN ING4(J,K)=ING4(J,K)-2*NRTT
2730 IF NRTT<MNEG4(J,K) THEN MNEG4(J,K)=NRTT
2740 RETURN
2750 NRTT5(J,K)=NRTT @ IF NRTT<0 THEN ING5(J,K)=ING5(J,K)+NRTT @ IN5(J,K)=IN5(J,
      )+SUB
2760 IF CLSN$ <> "Y" THEN 2780
2770 IF NRTT<0 THEN ING5(J,K)=ING5(J,K)-2*NRTT
2780 IF NRTT<MNEG5(J,K) THEN MNEG5(J,K)=NRTT
2790 RETURN
2800 NRTT6(J,K)=NRTT @ IF NRTT<0 THEN ING6(J,K)=ING6(J,K)+NRTT @ IN6(J,K)=IN6(J,
      K)+SUB
2810 IF CLSN$ <> "Y" THEN 2830

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2820 IF NRIT<0 THEN TNG6(J,K)=TNG6(J,K)-2*NRIT
2830 IF NRIT<MNEG6(J,K) THEN MNEG6(J,K)=NRIT
2840 RETURN
2850 NRIT7(J,K)=NRIT IF NRIT<0 THEN TNG7(J,K)=TNG7(J,K)+NRIT IF TNG7(J,K)=TNG7(J,
)+SUB
360 IF NRIT<MNEG7(J,K) THEN MNEG7(J,K)=NRIT
370 IF CLNS<>"Y" THEN 2890
380 IF NRIT<0 THEN TNG7(J,K)=TNG7(J,K)-2*NRIT
390 RETURN
360 NRIT8(J,K)=NRIT IF NRIT<0 THEN TNG8(J,K)=TNG8(J,K)+NRIT IF TNG8(J,K)=TNG8(J,
)+SUB
310 IF NRIT<MNEG8(J,K) THEN MNEG8(J,K)=NRIT
320 IF CLNS<>"Y" THEN 2940
330 IF NRIT<0 THEN TNG8(J,K)=TNG8(J,K)-2*NRIT
340 RETURN
350 NRIT9(J,K)=NRIT IF NRIT<0 THEN TNG9(J,K)=TNG9(J,K)+NRIT IF TNG9(J,K)=TNG9(J,
)+SUB
360 IF NRIT<MNEG9(J,K) THEN MNEG9(J,K)=NRIT
370 IF CLNS<>"Y" THEN 2990
380 IF NRIT<0 THEN TNG9(J,K)=TNG9(J,K)-2*NRIT
390 RETURN
105 ! *****
106 ! THE COLLISION ALGORITHM.
107 ! LINES 3020 - 3300
108 ! *****
120 AFS="F" IF INT ((PDS(SOURCE)-SAVESM*1000)/100)=INT (SAVESM/100) THEN AFS=
" " PRINTER IS 701
121 IF AFS="I" THEN PRINT "TRANSMISSION ON SAME A400 SOURCE/DEST = ":PDS(SOURCE
& CLNS="N" IF 3260
122 IF CLNS="Y" THEN 3090
125 IF ABS (SMALL-SMALL2)<= .001 THEN PRINTER IS 701 ELSE GOTO 3260
140 PRINT "A COLLISION OCCURRED BETWEEN ";SAVESM;" AND ";SAVESM2
150 I=INT (SAVESM/100) L=INT (SAVESM2/100)
160 IF W(I)<= W(L) THEN 3090
170 SAVEIJK=SAVESM2 NUMDEST=0 IF SAVEATR=W(L) IF TIBG=TTGB+W(L) IF TIMESTT(SOURC
)=TIMESTT(SOURCE)-1 & CLNS="Y"
180 PROB=0 IF CLNS="Y" IF GOSUB 2320 IF CLNS="N" IF GOTO 2010
190 CN=CN+1 IF PRINT "COLLISION NUMBER ":CN
100 I=INT (SAVESM/100) J=INT (SAVESM/10)-I*10 IF K=SAVESM-I*100-J*10
3110 ON I GOSUB 3150,3160,3170,3180,3190,3200,3210,3220,3230
3120 I=INT (SAVESM2/100) J=INT (SAVESM2/10)-I*10 IF K=SAVESM2-I*100-J*10
3130 ON I GOSUB 3150,3160,3170,3180,3190,3200,3210,3220,3230
3140 GOTO 3240

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3150 COL1(J,K)=COL1(J,K)+1 @ RETURN
3160 COL2(J,K)=COL2(J,K)+1 @ RETURN
3170 COL3(J,K)=COL3(J,K)+1 @ RETURN
3180 COL4(J,K)=COL4(J,K)+1 @ RETURN
3190 COL5(J,K)=COL5(J,K)+1 @ RETURN
3200 COL6(J,K)=COL6(J,K)+1 @ RETURN
3210 COL7(J,K)=COL7(J,K)+1 @ RETURN
3220 COL8(J,K)=COL8(J,K)+1 @ RETURN
3230 COL9(J,K)=COL9(J,K)+1 @ RETURN
3240 IF CLNS="Y" THEN DT(SOURCE)=T(SOURCE) @ CLNS="N" ELSE DT(SOURCE)=T(SOURCE)+
(I) @ CLNS="N"
3250 GOTO 3300
3260 DT(SOURCE)=T(SOURCE)
3270 PRINT "NO COLLISION OCCURED " @ IF PR=1 THEN PRINTER IS 1
3280 IF PR=3 THEN 3290 ELSE 3300
3290 IF ITERNUM>= NUMITER-NUMCYCLES THEN 3300 ELSE PRINTER IS 1
3300 PRINT "DATA TRANSFER ";DT(SOURCE) @ IF AFS="T" THEN AFS="F" @ GOTO 3320
3310 BBT=BBT+DT(SOURCE)
3320 PRINT " " @ PRINT "PORT, DEVICE, SOURCE ACCUMULATED WAITING TIMES SINCE
THE LAST TRANSMISSION"
3330 FOR I=1 TO HY @ FOR J=1 TO DV(I) @ FOR K=1 TO SR(I,J)
3340 IF I*100+J*10+K=SAVEIJK THEN TLST=0 ELSE GOTO 3370
3350 ON I GOSUB 1850,1860,1870,1880,1890,1900,1910,1920,1930
3360 GOTO 3390
3370 TTGBSLT=DT(SOURCE) @ TLST=999999
3380 ON I GOSUB 2210,2220,2230,2240,2250,2260,2270,2280,2290
3390 PRINT I; " ";J; " ";K;TAB (30);TLST
3400 NEXT K @ NEXT J @ NEXT I
3410 TTGB=TTGB+DT(SOURCE)
3420 PRINT "DATA TRANSFER COMPLETED: TOTAL TIME GONE BY ";TTGB
3430 GOSUB 2310
3440 PRINT "NEXT TRANSMISSION REQUEST BY ";SAVESM @ SAVEIJK=SAVESM
3450 ITNR=SMALL
3460 IF ITNR<0 THEN 3480
3470 TTGB=TTGB+ITNR
3480 TTGBSLT=ITNR @ SAVESM2=0 @ SMALL=0 @ SMALL2=0
3481 ! PRINT OUT CURRENT ENTRY DATA - EITHER FROM FILE OR USER ENTERED
3490 PRINT "TOTAL TIME THAT WILL BE GONE BY WHEN ";SAVESM;" STARTS TO TRANSMIT "
;TTGB
3500 NUMDEST=0 @ PROB=0 @ SAVEATR=TTNR @ SAVESM=0
3505 ! *****
3506 ! SOURCE DUMP ALGORITHM LINES 3510 - 3590
3507 ! *****

```

3810 PRINT "TOTAL TIME GONE BY = "; TTGB

```

320 PRINT "BUS BUSY TIME = ";BBT
330 END
340 ! *****
345 ! PRINT OUT CURRENT DATA - EITHER FROM FILE OR ENTERED BY USER
346 ! LINES 3850 - 4200
347 ! *****
350 PRINTER IS 701
360 PRINT "# OF HYPERCHANNEL PORTS = ";HY & PRINT " "
370 FOR I=1 TO HY & PRINT "# OF DEVICES FOR PORT";I;" = ";DV(I) & NEXT I & I=1
    PRINT " "
380 FOR K=1 TO DV(I)
390 PRINT "# OF SOURCES FOR PORT ";I;" DEVICE ";K;" = ";SR(I,K)
400 NEXT K & I=I+1 & IF I<= HY THEN 3880 ELSE I=0 & PRINT " "
410 PRINT "PORT,DEVICE,SOURCE          * OF BYTES ACCU
    AVERAGE ARRIVAL RATE
    JLATED"
420 I=I+1 & FOR J=1 TO DV(I) & FOR K=1 TO SR(I,J)
430 ON I GOSUB 480,520,560,600,640,680,720,760,800
440 PRINT I;" ";J;" ";K;TAB (35);LA;TAB (60);QQ
450 NEXT K & NEXT J
460 IF I=HY THEN 3970 ELSE GOTO 3920
470 PRINT " " & PRINT " " & FOR I=1 TO HY & FOR J=1 TO DV(I)
480 PRINT "PROPAGATION DISTANCE BETWEEN PORT ";I;" AND DEVICE ";J;" = ";D(I,J)
490 NEXT J & NEXT I & PRINT " "
500 FOR I=1 TO HY
510 PRINT "WAIT TIME OF PORT ";I;" BEFORE TRANSMISSION REQUEST (IN EVENT OF A C
    LLISION) ";W(I)
520 NEXT I
530 PRINT " " & PRINT " " & PRINT "CHANNEL SETUP AND RELEASE TIME = ";ISR & PRI
    T " "
540 PRINT "CHANNEL DATA TRANSFER RATE = ";CR
550 PRINT " " & PRINT "PORT,DEVICE          TRANSFER RATE          LOAD T
    MES "
560 FOR I=1 TO HY & FOR J=1 TO DV(I)
570 PRINT I;" ";J;TAB (30);R(I,J);TAB (54);LI(I,J)
580 NEXT J & NEXT I
590 PRINT " " & PRINT "COMBINATIONS (IJK - LMN) PROBABILITY TIME TO TRANSMIT
    (IJK) BYTES (IJK - LMN)"
600 FOR I=1 TO NUMPROB
610 PRINT PDS(I);TAB (30);PRO(I);TAB (40);T(I)
620 NEXT I
630 PRINT " " & PRINT "PORT,DEVICE,SOURCE          AVERAGE TIME TO NEXT TRANSMISSION
    REQUEST "
640 FOR I=1 TO HY & FOR J=1 TO DV(I) & FOR K=1 TO SR(I,J)
650 ON I GOSUB 480,520,560,600,640,680,720,760,800

```

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```

4150 PRINT I;" ";J;" ";K:IAB (30);AIR
4170 NEXT K @ NEXT J @ NEXT I
4180 PRINT " "; @ PRINT "IJ-LM DATA FILE DUMP   BYTES TO BE DUMPED   TIME BETWEEN
DUMPS (IN SECONDS)"
190 FOR I=1 TO SNUM @ PRINT DFL(I);IAB (30);BYTES(I);IAB (50);TIM(I)
200 NEXT I @ GOTO 1720
210 ! *****
215 ! THE EDIT ROUTINE - SAVES CORRECTED DATA TO FILE
216 ! LINES 4220 - 4950
217 ! *****
220 CLEAR @ DISP "      EDIT MENU" @ DISP " "
230 DISP "1  -- AVERAGE ARRIVAL RATES"
240 DISP "2  -- NUMBER OF BYTES ACCUMULATED"
250 DISP "3  -- PROPAGATION DISTANCES"
260 DISP "4  -- WAIT TIMES"
270 DISP "5  -- CHANNEL SETUP AND RELEASE TIME"
280 DISP "6  -- CHANNEL DATA TRANSFER RATE"
290 DISP "7  -- TRANSFER RATES"
300 DISP "8  -- PROBABILITY DATA"
310 DISP "9  -- SOURCE DATA DUMPS"
320 DISP "10 -- TERMINATE EDIT MODE"
330 DISP " " @ DISP "ENTER YOUR SELECTION "; @ INPUT EDIT
340 IF EDIT<1 OR EDIT>10 THEN 4210
350 ON EDIT GOTO 4360,4450,4590,4630,4660,4690,4760,4850,4950
360 FOR I=1 TO HY @ CLEAR @ FOR J=1 TO DV(I) @ FOR K=1 TO SR(I,J) @ CLEAR
370 ON I GOSUB 480,520,560,600,640,680,720,760,800
380 DISP "PORT ";I;" DEVICE ";J;" SOURCE ";K @ DISP @ DISP
390 LA(J,K)=LA @ QQ(J,K)=QQ
400 DISP "AVERAGE ARRIVAL RATE = ";LA
410 QS=" " @ DISP " " @ DISP "PRESS ENTER TO RETAIN CURRENT DATA OR
ENTER THE CORRECT VALUE "; @ INPUT QS
420 IF QS<>" " THEN LA(J,K)=VAL (QS)
430 NEXT K @ NEXT J @ ON I GOSUB 450,490,530,570,610,650,690,730,770
440 NEXT I @ GOTO 4210
450 FOR I=1 TO HY @ CLEAR @ FOR J=1 TO DV(I) @ FOR K=1 TO SR(I,J) @ CLEAR
460 ON I GOSUB 480,520,560,600,640,680,720,760,800
470 DISP "PORT ";I;" DEVICE ";J;" SOURCE ";K @ DISP @ DISP
480 QQ(J,K)=QQ @ LA(J,K)=LA
490 DISP "NUMBER OF BYTES ACCUMULATED = ";QQ
500 QS=" " @ DISP " " @ DISP "PRESS ENTER TO RETAIN CURRENT DATA OR
ENTER THE CORRECT VALUE "; @ INPUT QS
510 IF QS<>" " THEN QQ(J,K)=VAL (QS)
4520 NEXT K @ NEXT J @ ON I GOSUB 450,490,530,570,610,650,690,730,770
4530 NEXT I @ GOTO 4840

```

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```

540 CLEAR @ FOR I=1 TO HY @ FOR J=1 TO DV(I) @ CLEAR
550 DISP "PROPAGATION DISTANCE BETWEEN PORT ";I;" AND DEVICE ";J;" IN MICROSEC
DS = "D(I,J) @ Q$="
560 DISP " " @ DISP "PRESS ENTER TO RETAIN CURRENT DATA OR ENTER THE CORRECT VA
UE " @ INPUT Q$
570 IF Q$ <> "" THEN D(I,J)=VAL (Q$)
580 NEXT J @ NEXT I @ GOTO 4210
590 CLEAR @ FOR I=1 TO HY @ CLEAR @ DISP "WAIT TIME OF PORT ";I;" BEFORE TRANSM
SSION REQUEST = ";W(I)
590 DISP " " @ Q$=" " @ DISP "PRESS ENTER TO RETAIN CURRENT DATA OR ENTER THE C
RECT VALUE " @ INPUT Q$
590 IF Q$ <> "" THEN W(I)=VAL (Q$)
590 NEXT I @ GOTO 4210
590 CLEAR @ DISP "CHANNEL SETUP AND RELEASE TIME = ";ISR
590 Q$=" " @ DISP "PRESS ENTER TO RETAIN CURRENT DATA OR ENTER THE P
PER VALUE " @ INPUT Q$ @ IF Q$ <> "" THEN ISR=VAL (Q$)
590 GOTO 4840
590 CLEAR @ DISP "CHANNEL DATA TRANSFER RATE = ";CR @ DISP " "
590 Q$=" " @ DISP "PRESS ENTER TO RETAIN CURRENT DATA OR ENTER THE PROPER VALUE
@ INPUT Q$ @ IF Q$ <> "" THEN CR=VAL (Q$)
590 GOTO 4840
590 CLEAR @ FOR I=1 TO HY @ FOR J=1 TO DV(I) @ CLEAR
590 DISP "RATE AT WHICH DATA IS TRANSFERED FROM DEVICE ";J;" TO PORT ";I;" = ";
I,J)
590 Q$=" " @ DISP " " @ DISP "PRESS ENTER TO RETAIN CURRENT DATA OR ENTER THE C
RECT VALUE " @ INPUT Q$
590 IF Q$ <> "" THEN R(I,J)=VAL (Q$)
590 LT(I,J)=1/R(I,J)
590 NEXT J @ NEXT I
590 GOTO 4840
590 CLEAR @ FOR I=1 TO NUMPROB
590 CLEAR @ DISP "(PORT,DEVICE,SOURCE) TRANSMITS TO (PORT,DEVICE,SOURCE) CODE
";PDS(I) @ DISP " "
590 Q$=" " @ DISP "PRESS ENTER TO RETAIN CURRENT DATA OR ENTER THE CORRECT VALU
" @ INPUT Q$
590 IF Q$ <> "" THEN PDS(I)=VAL (Q$)
590 DISP " " @ DISP "PROBABILITY = ";PRJ(I) @ DISP " "
590 Q$=" " @ DISP "PRESS ENTER TO RETAIN CURRENT DATA OR ENTER THE CORRECT VALU
" @ INPUT Q$
590 IF Q$ <> "" THEN PRJ(I)=VAL (Q$)

```



```

1830 NEXT I
1840 FOR I=1 TO NUMPROB : GOSUB 1160 : NEXT I : GOTO 4210
1850 CLEAR : FOR I=1 TO SNUM : CLEAR : DISP "(I) - I)" SOURCE DATA FILE DUMPS TO
    DESTINATION LM. CODE = ":DTFL(I) : DISP "
1860 Q$=" " : DISP "PRESS ENTER TO RETAIN CURRENT DATA OR ENTER THE CORRECT VALU
    " : INPUT Q$
1870 IF Q$ <> "" THEN DTFL(I)=VAL (Q$)
1880 DISP " " : DISP "# OF BYTES TO BE DUMPED ":BYTES(I) : DISP " "
1890 Q$=" " : DISP "PRESS ENTER TO RETAIN CURRENT DATA OR ENTER THE CORRECT VALU
    " : INPUT Q$
1900 IF Q$ <> "" THEN BYTES(I)=VAL (Q$)
1910 DISP " " : DISP "TIME INTERVAL BETWEEN DUMPS ":TIM(I) : DISP " "
1920 Q$=" " : DISP "PRESS ENTER TO RETAIN CURRENT DATA OR ENTER THE CORRECT VALU
    " : INPUT Q$
1930 IF Q$ <> "" THEN TIM(I)=VAL (Q$)
1940 NEXT I : GOTO 4210
1950 GETDATA=888 : ON AN GOTO 1350.1720.1350

```

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APPENDIX II

PASCAL SIMULATIONS ALGORITHM PROGRAM

A listing of the PASCAL simulation algorithm program is presented in this appendix. The program contains comments, but is not considered to be user oriented at this point. This program is FORTRAN based, but requires software support for PASCAL in the host computer.

APAS, IS
Pascal (ver. 2.0ILR, updated 09/24/81) - 07/01/83 16:39:06

APAS-15	(ver. 2.0ILR, updated 09/24/81) - 07/01/83 16:39:06
Pascal	000000 --
1	000000 --
2	000000 --
3	000000 --
4	000000 --
5	000000 --
6	000000 --
7	000000 --
8	000000 --
9	000000 --
10	000000 --
11	000000 --
12	000000 --
13	000000 --
14	000000 --
15	000000 --
16	000000 --
17	000000 --
18	000000 --
19	000000 --
20	000000 --
21	000000 --
22	000000 --

PROGRAM MOD1(INPUT,OUTPUT,TERMINAL,DESCRIP FILE,AUX OUT):

(.....)
 (.....)
 (.....) DATA DECLARATIONS
 (*)

```

const
  MAX_NUM_SOURCES      = 1;
  MAX_NUM_ADAPTERS     = 1;
  NUM_OF_ADAPTERS      = 1;
  LPP                  = 1;
  TRUNK_DEV            = 1;
  TRUNK_OVERHEAD        = 0.00025; (* Trunk set-up and release time *)
  TRUNK_RATE           = 50E6; (* Trunk tx rate in Bits/second *)
  ONE                   = 1;
  ZERO                 = 0;
  COLL_EPS             = 0.001; (* Increment to determine collisions *)

```

```

TYPE SOURCE DESCRIPTIONS = record
  TX RATE      : real ; (* Avg number of bytes/sec *)
end;

```

```

DEVICE_RECORD = record
OPEN_          : boolean;      (* TRUE if device attached to port *)
DEV_NUM       : integer;      (* Device number *)
BUFFER_SIZE   : integer;      (* Number of bits accumulated from a
                                off-net source before the device requests
                                a trunk transmission *)

```

```

NUM OF SOURCES : integer;
SOURCE : array[1..MAX_NUM_SOURCES] of SOURCE_DESCRIPTIONS;
TRANSFER_RATE : real;
    (* I/O port transfer rate from dev to dev in bytes/sec *)
LOAD_TIME : real;
    (* inverse of TFRM_RATE = time for dev to load adapt buffer *)
WCTM : real;
    (* time until next transmissions *)

```

```

LAST TRUNK_TX      == real;
TX_A              == real;
TX_INTRVL         == integer;
TX_CT             == integer;
WAIT_CT           == integer;
COLL_CT           == integer;
COLL_TX           == integer;
RX_TX_TIME        == real;
TX_TIME           == real;

(* time between tx requests based on aggregate submission rate *)
(* device transmission rate *)
(* device waiting time *)
(* device collision rate *)
(* device receiving rate *)
(* time device spent in transmissions *)

```

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```
WAIT_TIME      : real;      (* Time device spent in wait delays *)
COLL_TIME      : real;      (* Time device spent in coll. waits *)
RX_TIME        : real;      (* Time device spent receiving trunk tx *)
end;
```

```
ADAPTER RECORDS = record
  PROP_DIST_TO : array[1..NUM_OF_ADAPTERS] of real; (* Number of seconds
  required to transmit from one adapter
  to each other adapter on the net *)
  PRIORITY_DELAY : real; (* Number of seconds of assigned wait time in
  event of wait during transmission *)
  END_DELAY : real; (* Wait time if wait flip flop enabled *)
  ATX_CT : integer; (* Tally of adapter transmissions *)
  AWAIT_CT : integer; (* Tally of adapter waits *)
  ACOLL_CT : integer; (* Tally of adapter collisions *)
  ATX_TIME : real; (* Time adapter spent transmitting *)
  AWAIT_TIME : real; (* Time adapter spent in wait delays *)
  ACOLL_TIME : real; (* Time adapter spent in collisions *)
end;
```

```
TEXTFILE = file of CHAR;
```

```
MODE_TYPE = (INTERACTIVE, FILE_INPUT);
```

```
CLOCK_COND = (NORMAL, COLLISION, OTHER);
```

```
var
  ADAPTER : array[1..NUM_OF_ADAPTERS] of ADAPTER_RECORDS;
  PR : array[1..TNUM_DEV, 1..TNUM_DEV] of real; (* Probability
  of transmission matrix *)
```

```
BITS_TX, TOTAL_ATTEMPTS,
```

```
TX_TALLY, TX,
```

```
WAIT_TALLY,
```

```
COLLISION_TALLY,
```

```
RETRY_TALLY,
```

```
I, J, K, D, NUM
```

```
: integer;
```

```
MAX_TIME,
```

```
CURRENT_TIME,
```

```
TIME_ACTIVE,
```

```
LA, TRUNK_TX
```

```
: real; (* Run time for prog. in simulation secs *)
```

```
RESP : integer; (* Time of last successful trunk transmission *)
```

```
DESCRIP_FILE, AVX_OUT : TEXTFILE;
```

```
INPUT_MODE : MODE_TYPE;
```

```
COND1 : array[CLOCK_COND] of CLOCK_COND;
```

```
COND2 : array[CLOCK_COND] of CLOCK_COND;
```

```
PRINT_ALL : boolean;
```

```
LC1, ACT, JK
```

```
: integer;
```

```
(.....)
```

```
(.....)
```

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(.....)
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(.....)
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(.....)
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(.....)
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(.....)
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```
00000000 0- A begin
00000001   with DEVICE do
00000002   TIME_TO_NEXT_TX := (CURRENT_TIME - LAST_TRUNK_TX) - TX_INTRVL;
00000003   A
00000004   end;
00000005
00000006  (* procedure PRINT_WAIT_STATS(TMTTR: DEVICE_RECORD);
00000007  (* Prints statistics on successful trunk tx.
00000008  *)
00000009  (*
00000010  *)
00000011  var ADAPT,
00000012  W_DEV : integer;
00000013
00000014  begin
00000015  W_DEV := TMTTR.DEV_NUM mod 10;
00000016  W_ADAPT := (TMTTR.DEV_NUM mod 100) div 10;
00000017
00000018  writeln(OUTPUT);
00000019  writeln(OUTPUT, "Time: ", current_time, " ");
00000020  writeln(OUTPUT, "Transmitter: ", adapter, " ");
00000021  writeln(OUTPUT, "Device: ", W_DEV, " ");
00000022  end; (* PRINT TX STATS *)
00000023
00000024  (*
00000025  *)
00000026  (* procedure PRINT_TX_STATS(TMTTR,RCVR: DEVICE_RECORD);
00000027  (* Prints statistics on successful trunk tx.
00000028  *)
00000029  (*
00000030  *)
00000031  var ADAPT,R_ADAPT,
00000032  T_DEV,R_DEV : integer;
00000033
00000034  begin
00000035  T_DEV := TMTTR.DEV_NUM mod 10;
00000036  T_ADAPT := (TMTTR.DEV_NUM mod 100) div 10;
00000037  R_DEV := RCVR.DEV_NUM mod 10;
00000038  R_ADAPT := (RCVR.DEV_NUM mod 100) div 10;
00000039
00000040  writeln(OUTPUT);
00000041  writeln(OUTPUT, "Time: ", current_time, " ");
00000042  writeln(OUTPUT, "Transmitter: ", adapter, " ");
00000043  writeln(OUTPUT, "Device: ", T_DEV, " ");
00000044  writeln(OUTPUT, "Receiver: ", R_DEV, " ");
00000045  end; (* PRINT TX STATS *)
00000046
00000047  (*
00000048  *)
00000049  (* procedure PRINT_COLLISION_STATS(TMTTR,RCVR: DEVICE_RECORD);
00000050  (* Prints stats on event of collision.
00000051  *)
00000052  (*
00000053  *)
00000054  var ADAPT,P_ADAPT,
00000055  T_DEV,R_DEV : integer;
00000056
00000057  begin
00000058  T_DEV := TMTTR.DEV_NUM mod 10;
00000059  T_ADAPT := (TMTTR.DEV_NUM mod 100) div 10;
00000060
```

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```

311 001256  -- C A      end; (* PRINT NETWORK STATS *)
312 001257  --
313 001258  --
314 001259  --
315 001260  -- A      (* function TRANSFER_TIME(SENDER, RECEIVER :
316 001261  --      (* calculates time required to transmit a message *)
317 001262  --      (* from a sender to a receiver *)
318 001263  --      (* Trunk overhead time includes the fixed delay for each *)
319 001264  --      (* adapter which is length of time required by a sending *)
320 001265  --      (* device to receive a response frame *)
321 001266  --      (* ***** *)
322 001267  --      var T, SLOWER_RATE: real;
323 001268  --      PKT_CT: integer;
324 001269  --
325 001270  --      begin
326 001271  --
327 001272  --      SLOWER_RATE := SENDER_TFER_RATE;
328 001273  --      if RECEIVER_TFER_RATE < SLOWER_RATE then
329 001274  --      SLOWER_RATE := RECEIVER_TFER_RATE;
330 001275  --
331 001276  --      PKT_CT := round(SENDER_BUFFER_SIZE/2048);
332 001277  --
333 001278  --      case PKT_CT of
334 001279  --      0: T:=TRUNK_OVERHEAD;
335 001280  --      1: T:= 2048/TRUNK_RATE + TRUNK_OVERHEAD;
336 001281  --      otherwise: T:= 2048/(SENDER_TFER_RATE + 2048/TRUNK_RATE +
337 001282  --      ((PKT_CT-2)*2048)/SLOWER_RATE + TRUNK_OVERHEAD
338 001283  --      end;
339 001284  --      TRANSFER_TIME:=T;
340 001285  --
341 001286  --      end;
342 001287  --
343 001288  --      (* ***** *)
344 001289  --      (* procedure UPDATE_CLOCKS(COND:clock COND):
345 001290  --      (* Updates both the system clocks and device *)
346 001291  --      (* clocks whenever a system action has *)
347 001292  --      (* occurred. *)
348 001293  --      (* ***** *)
349 001294  --      var
350 001295  --      TA, TO, RA, RD:
351 001296  --      I, J: integer;
352 001297  --      DELAY_TIME: real;
353 001298  --      TIME, TI: real;
354 001299  --
355 001300  --      begin
356 001301  --      (* A is transmitter adapter no., D is device no. *)
357 001302  --
358 001303  --      TA := (MITR.DEV_NUM mod 100) div 10;
359 001304  --      RA := (RCVR.DEV_NUM mod 100) div 10;
360 001305  --      TD := IMITR.DEV_NUM mod 10;
361 001306  --      RD := RCVR.DEV_NUM mod 10;
362 001307  --
363 001308  --      case COND of
364 001309  --      NORMAL
365 001310  --      TIME := TRANSFER_TIME(MITR, RCVR);
366 001311  --      BITS_IX:=BITS_IX+ IMITR_BUFFER_SIZE;
367 001312  --
368 001313  --      (* Update system clock *)
369 001314  --      CURRENT_TIME:= CURRENT_TIME+TIME;
370 001315  --
371 001316  --      (* Update each device clock *)
372 001317  --
373 001318  --
374 001319  --
375 001320  --
376 001321  --
377 001322  --
378 001323  --
379 001324  --
380 001325  --
381 001326  --
382 001327  --
383 001328  --
384 001329  --
385 001330  --
386 001331  --
387 001332  --
388 001333  --
389 001334  --
390 001335  --

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```

391 for J:= 1 to NUM_OF_ADAPTERS do
392   for J:=1 to 4 do
393     with ADAPTER[I].DEVICE[J] do
394       if (not OPEN) and (NOT(CI=1) and (ID=J))) then
395         begin
396           if NEXT_TX <= CURRENT_TIME then
397             begin
398               DELAY_TIME:=ADAPTER[I].PRIORITY_DELAY + ADAPTER[I].END_DELAY;
399               NEXT_TX:=CURRENT_TIME + DELAY_TIME;
400               WAIT-TALLY:=WAIT-TALLY + 1;
401               WAIT-CT:=WAIT-CT + 1;
402               WAIT_TIME:=WAIT_TIME+DELAY_TIME+TIME;
403             end
404           if PRINT ALL then
405             PRINT WAIT_STAT$(ADAPTER[I].DEVICE[J]);
406           end; (* NEXT_TX <= CURRENT_TIME *)
407         end;
408       (* Responder transmitter clock *)
409       with ADAPTER[TA].DEVICE[ID] do
410         begin
411           NEXT_TX:= CURRENT_TIME+ TX INTRVL;
412           LAST-TRUNK TX:=CURRENT_TIME;
413           TX-CT:=TX-CT + 1;
414           TX_TIME:=TX_TIME + TIME;
415           end; (* with ADAPTER[TA].DEVICE[ID] *)
416         with ADAPTER[RA].DEVICE[RD] do
417           begin
418             RX-CT:=RX-CT + 1;
419             RX_TIME:=RX_TIME+TIME;
420             end; (* with ADAPTER[RA].DEVICE[RD] *)
421           TIME_ACTIVE:=TIME_ACTIVE+TRANSFER_TIME(TMITTR,RCVR);
422         end; (* NORMAL case *)
423       end;
424     COLLISION : begin
425       with ADAPTER[TA].DEVICE[TD] do
426         begin
427           TX:= COLL_EPS + (UNIFORM(ZERO,ONE,U))/(1.0/COLL_EPS));
428           NEXT_TX:=NEXT_TX + TX;
429           COLL-CT:=COLL-CT+1;
430           COLL_TIME:=COLL_TIME+ TX;
431           end; (* with ADAPTER[TA].DEVICE[TD] *)
432         end; (* COLLISION case *)
433       OTHER : begin
434         end; (* OTHER case *)
435       end; (* A case *)
436     end; (* UPDATE CLOCKS *)
437   end;
438   (* function TX_INTERVAL(DEVICE : DEVICE RECORD) : real;
439   (* calculates the time interval between requests *)
440   (* for trunk transmissions. Based on the amount *)
441   (* of data (rate) received by a device from an *)
442   (* of net sources and the size of the device *)
443   (* buffer. *)
444   (* ===== *)
445   var
446     I : integer;
447     AGGREGATE_RATE : real;
448   begin
449     AGGREGATE_RATE := 0.00;
450     for I := 1 to DEVICE.NUM_OF_SOURCES do
451

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```
711 004 0 71
712 004 0 71
713 004 1 27
714 004 1 27
715 004 1 35
716 004 1 44
717 004 1 44
718 004 1 47
719 004 1 47
720 004 1 53
721 004 1 54
722 004 1 64
723 004 1 64
724 004 1 70
725 004 1 70
726 004 1 72
727 004 1 72
728 004 1 72
729 004 1 72
730 004 1 72
731 0000006
732 0000006
733 0000006
734 0000006
735 0000006
736 000010
737 000010
738 000010
739 000010
740 000010
741 0000006
742 0000006
743 0000006
744 0000006
745 0000006
746 0000006
747 0000006
748 0000006
749 0000006
750 0000006
751 0000006
752 0000006
753 0000006
754 0000010
755 0000010
756 0000014
757 0000014
758 000012
759 000012
760 004 2 02
761 004 2 02
762 004 2 02
763 004 2 05
764 004 2 06
765 004 2 03
766 004 2 03
767 004 2 03
768 004 2 03
769 004 2 03
770 004 2 03
771 004 2 50
772 004 2 50
773 004 2 76
774 004 3 22
775 004 3 22
776 004 3 22
777 004 3 22
778 004 3 22
779 004 3 22
780 004 3 41
781 004 3 41
782 004 3 65
783 004 3 65
784 004 3 76
785 004 3 76
786 004 4 02
787 004 4 02
788 004 4 11
789 004 4 11
790 004 4 24

write(AUX_OUT,1:4,J:=4,PC_ACT_TR:=11:2,PC_TX_TR:=14:2);
write(AUX_OUT,PC_TX_DEV:=14:2,PC_WAIT_TR:=14:2,PC_WAIT_DEV:=14:2);
write(AUX_OUT,PC_COLL_TR:=14:2);
write(AUX_OUT,PC_COLL_DEV:=14:2);
write(AUX_OUT);

LCI:=LCI+2;
end; (* not OPEN *)

NEUPAGE;
end; (* procedure ACTIVITY SUMMARY *)

A
end;

(* procedure CHARACTERIZE_NETWORK;
(* prompts for network descriptive info.
(* .....
*)
*)
var
I,J : integer;
(* .....
*)
(* procedure INITIALIZE_PROB_MATRIX;
(* initializes matrix containing the probability
(* that any device will request a transmission to
(* any other device. Matrix is a 2-D array.
(* Row references refer to sender and columns
(* refer to receiver. Each device is assigned a
(* unique device number upon program startup.
(* and these numbers should be used to obtain
(* the probabilities.
(* .....
*)
*)
var
C,P : real;
I,J,K : integer;
begin
C:=0; ROW:=0;
case INPUT MODE of
INTERACTIVE : begin
write(OUTPUT);
write(OUTPUT);
write(OUTPUT);
write(OUTPUT);
write(OUTPUT);
for I:=1 to NUM_OF_ADAPTERS do
for J:=1 to 4 do
with ADAPTER[I].DEVICE[J] do
if not OPEN then
write(OUTPUT,1:5,J:1);
write(OUTPUT);
end;
for I:=1 to NUM_OF_ADAPTERS do
for J:=1 to 4 do
with ADAPTER[I].DEVICE[J] do
if not OPEN then
begin
write(TERMINAL,1:2,J:1);
ROW:=ROW+1;
C:=C+N:=1;
while K<=6_NUM do
begin
read(TERMINAL,P);
if (C+P) <= 1 then
begin
```

```

064424 791 PR(ROW,K) := (+P;
064444 792 C:=C+P;
064450 793 K:=K+1;
064454 794 end;
064458 795 writeln(OUTPUT, ' ** ERROR ** REENTER LINE, TOTAL PERCENT CANT BE > 100 ');
064462 796 end;
064466 797 while K <= D_NUM *
064470 798 readln(TERMINAL);
064474 799 end; (* not OPEN *)
064478 800 end; (* I INTERACTIVE case *)
064482 801
064486 802 FILE INPUT : begin
064490 803 for I:= 1 to NUM_OF_ADAPTERS do
064494 804 for J:= 1 to 4 do
064498 805 with ADAPTER[I], DEVICE[J] do
064502 806 if not OPEN then
064506 807 begin
064510 808 ROW:=ROW+1;
064514 809 for K:= 1 to D_NUM do
064518 810 read(DSCRIP-FILE, PR(ROW,K));
064522 811 readln(DSCRIP-FILE);
064526 812 end; (* for I:= -1 D_NUM *)
064530 813 end; (* FILE case *)
064534 814 OTHERWISE : end;
064538 815
064542 816 end; (* procedure to initialize cumulative prob matrix *)
064546 817
064550 818 0 B
064554 819
064558 820 0 A begin (* procedure CHARACTERIZE NETWORK *)
064562 821 writeln(TERMINAL, 'Input mode: INTERACTIVE or FILE');
064566 822 readln(TERMINAL, RESP);
064570 823 if (RESP[1] = 'I') or (RESP[1] = 'i') then
064574 824 INPUT_MODE := INTERACTIVE
064578 825 else
064582 826 INPUT_MODE := FILE_INPUT;
064586 827
064590 828 (* Begin description of network by describing adapters *)
064594 829
064598 830 CASE INPUT_MODE OF
064602 831 INTERACTIVE : begin
064606 832 writeln('*****');
064610 833 writeln('*****');
064614 834 writeln('*****');
064618 835 writeln('*****');
064622 836 writeln('*****');
064626 837 writeln('*****');
064630 838 writeln('*****');
064634 839 writeln('*****');
064638 840 writeln('*****');
064642 841 writeln('*****');
064646 842 writeln('*****');
064650 843 writeln('*****');
064654 844 writeln('*****');
064658 845 writeln('*****');
064662 846 writeln('*****');
064666 847 writeln('*****');
064670 848 writeln('*****');
064674 849 writeln('*****');
064678 850 writeln('*****');
064682 851 writeln('*****');
064686 852 writeln('*****');
064690 853 writeln('*****');
064694 854 writeln('*****');
064698 855 writeln('*****');
064702 856 writeln('*****');
064706 857 writeln('*****');
064710 858 writeln('*****');
064714 859 writeln('*****');
064718 860 writeln('*****');
064722 861 writeln('*****');
064726 862 writeln('*****');
064730 863 writeln('*****');
064734 864 writeln('*****');
064738 865 writeln('*****');
064742 866 writeln('*****');
064746 867 writeln('*****');
064750 868 writeln('*****');
064754 869 writeln('*****');
064758 870 writeln('*****');
064762 871 writeln('*****');
064766 872 writeln('*****');
064770 873 writeln('*****');
064774 874 writeln('*****');
064778 875 writeln('*****');
064782 876 writeln('*****');
064786 877 writeln('*****');
064790 878 writeln('*****');
064794 879 writeln('*****');
064798 880 writeln('*****');
064802 881 writeln('*****');
064806 882 writeln('*****');
064810 883 writeln('*****');
064814 884 writeln('*****');
064818 885 writeln('*****');
064822 886 writeln('*****');
064826 887 writeln('*****');
064830 888 writeln('*****');
064834 889 writeln('*****');
064838 890 writeln('*****');
064842 891 writeln('*****');
064846 892 writeln('*****');
064850 893 writeln('*****');
064854 894 writeln('*****');
064858 895 writeln('*****');
064862 896 writeln('*****');
064866 897 writeln('*****');
064870 898 writeln('*****');
064874 899 writeln('*****');
064878 900 writeln('*****');
064882 901 writeln('*****');
064886 902 writeln('*****');
064890 903 writeln('*****');
064894 904 writeln('*****');
064898 905 writeln('*****');
064902 906 writeln('*****');
064906 907 writeln('*****');
064910 908 writeln('*****');
064914 909 writeln('*****');
064918 910 writeln('*****');
064922 911 writeln('*****');
064926 912 writeln('*****');
064930 913 writeln('*****');
064934 914 writeln('*****');
064938 915 writeln('*****');
064942 916 writeln('*****');
064946 917 writeln('*****');
064950 918 writeln('*****');
064954 919 writeln('*****');
064958 920 writeln('*****');
064962 921 writeln('*****');
064966 922 writeln('*****');
064970 923 writeln('*****');
064974 924 writeln('*****');
064978 925 writeln('*****');
064982 926 writeln('*****');
064986 927 writeln('*****');
064990 928 writeln('*****');
064994 929 writeln('*****');
064998 930 writeln('*****');
065002 931 writeln('*****');
065006 932 writeln('*****');
065010 933 writeln('*****');
065014 934 writeln('*****');
065018 935 writeln('*****');
065022 936 writeln('*****');
065026 937 writeln('*****');
065030 938 writeln('*****');
065034 939 writeln('*****');
065038 940 writeln('*****');
065042 941 writeln('*****');
065046 942 writeln('*****');
065050 943 writeln('*****');
065054 944 writeln('*****');
065058 945 writeln('*****');
065062 946 writeln('*****');
065066 947 writeln('*****');
065070 948 writeln('*****');
065074 949 writeln('*****');
065078 950 writeln('*****');
065082 951 writeln('*****');
065086 952 writeln('*****');
065090 953 writeln('*****');
065094 954 writeln('*****');
065098 955 writeln('*****');
065102 956 writeln('*****');
065106 957 writeln('*****');
065110 958 writeln('*****');
065114 959 writeln('*****');
065118 960 writeln('*****');
065122 961 writeln('*****');
065126 962 writeln('*****');
065130 963 writeln('*****');
065134 964 writeln('*****');
065138 965 writeln('*****');
065142 966 writeln('*****');
065146 967 writeln('*****');
065150 968 writeln('*****');
065154 969 writeln('*****');
065158 970 writeln('*****');
065162 971 writeln('*****');
065166 972 writeln('*****');
065170 973 writeln('*****');
065174 974 writeln('*****');
065178 975 writeln('*****');
065182 976 writeln('*****');
065186 977 writeln('*****');
065190 978 writeln('*****');
065194 979 writeln('*****');
065198 980 writeln('*****');
065202 981 writeln('*****');
065206 982 writeln('*****');
065210 983 writeln('*****');
065214 984 writeln('*****');
065218 985 writeln('*****');
065222 986 writeln('*****');
065226 987 writeln('*****');
065230 988 writeln('*****');
065234 989 writeln('*****');
065238 990 writeln('*****');
065242 991 writeln('*****');
065246 992 writeln('*****');
065250 993 writeln('*****');
065254 994 writeln('*****');
065258 995 writeln('*****');
065262 996 writeln('*****');
065266 997 writeln('*****');
065270 998 writeln('*****');
065274 999 writeln('*****');
065278 1000 writeln('*****');
065282 1001 writeln('*****');
065286 1002 writeln('*****');
065290 1003 writeln('*****');
065294 1004 writeln('*****');
065298 1005 writeln('*****');
065302 1006 writeln('*****');
065306 1007 writeln('*****');
065310 1008 writeln('*****');
065314 1009 writeln('*****');
065318 1010 writeln('*****');
065322 1011 writeln('*****');
065326 1012 writeln('*****');
065330 1013 writeln('*****');
065334 1014 writeln('*****');
065338 1015 writeln('*****');
065342 1016 writeln('*****');
065346 1017 writeln('*****');
065350 1018 writeln('*****');
065354 1019 writeln('*****');
065358 1020 writeln('*****');
065362 1021 writeln('*****');
065366 1022 writeln('*****');
065370 1023 writeln('*****');
065374 1024 writeln('*****');
065378 1025 writeln('*****');
065382 1026 writeln('*****');
065386 1027 writeln('*****');
065390 1028 writeln('*****');
065394 1029 writeln('*****');
065398 1030 writeln('*****');
065402 1031 writeln('*****');
065406 1032 writeln('*****');
065410 1033 writeln('*****');
065414 1034 writeln('*****');
065418 1035 writeln('*****');
065422 1036 writeln('*****');
065426 1037 writeln('*****');
065430 1038 writeln('*****');
065434 1039 writeln('*****');
065438 1040 writeln('*****');
065442 1041 writeln('*****');
065446 1042 writeln('*****');
065450 1043 writeln('*****');
065454 1044 writeln('*****');
065458 1045 writeln('*****');
065462 1046 writeln('*****');
065466 1047 writeln('*****');
065470 1048 writeln('*****');
065474 1049 writeln('*****');
065478 1050 writeln('*****');
065482 1051 writeln('*****');
065486 1052 writeln('*****');
06
```

```

371 writeIn(TERMINAL, '13, end delay in sec is ');
372 readIn(TERMINAL, END_DELAY);
373
374 (* Begin individual device descriptions *)
375 for J:= 1 to 4 do
376 begin
377   with ADAPTER[J]-DEVICE[J] do
378   begin
379     OPEN:= true;
380     writeIn('2, Adapter, 1-2, Device, 1-2, description');
381     readIn(TERMINAL, RESP);
382     if (RESP[1]='C') or (RESP[1]='c') then
383       OPEN:= false;
384       if not(OPEN) then
385       begin
386         NUM:=0; NUM+1;
387         DEV_NUM:=DEV_NUM+100 + 1+10 *J;
388         writeIn(TERMINAL, '4, Buffer size (Bytes) ');
389         (* Change bytes to bits for storage *)
390         readIn(TERMINAL, BUFFER_SIZE);
391         BUFFER_SIZE:=BUFFER_SIZE*8;
392         writeIn(TERMINAL, '4, 1/0 bus transfer rate, (Bps)');
393         readIn(TERMINAL, TFR_RATE);
394         TFR_RATE:=TFR_RATE*8;
395         LOAD_TIME:=1.0/TFR_RATE;
396         writeIn(TERMINAL, '4, Number of offnet sources for device:');
397         readIn(TERMINAL, NUM_OF_SOURCES);
398         for K:=1 to NUM_OF_SOURCES do
399         begin
400           writeIn(TERMINAL, '6, Source #, 2, transmission rate, (bps)');
401           readIn(TERMINAL, SOURCE[K], TX_RATE);
402           SOURCE[K].TX_RATE:=SOURCE[K].TX_RATE*8;
403           end; (* for K:=1 to NUM_OF_SOURCES *)
404           TX_INTERVAL:= TX_INTERVAL * (1.0 / NUM_OF_SOURCES);
405           NEXT_TX:= TX_INTERVAL * (ADAPTER[J].DEVICE[J]);
406           end; (* if not(OPEN) *)
407           end; (* for J:=1 to 4 *)
408           INITIALIZE FROM MATRIX;
409           end; (* INTERACTIVE case *)
410
411 FILE_INPUT := begin
412   reset(DESCRIP_FILE);
413   for I:= 1 to NUM_OF_ADAPTERS do
414   begin
415     with ADAPTER[I] do
416     begin
417       J:=1;
418       while J<=1 do
419       begin
420         readIn(DESCRIP_FILE, PROP_DIST_10[J]);
421         J:=J+1;
422         end; (* while J<=1 *)
423         if J=1 then J:=J+1;
424         while (J>1) and (J<=MAX_NUM_ADAPTERS) do
425         begin
426           readIn(DESCRIP_FILE, PROP_DIST_10[J]);
427           J:=J+1;
428           end; (* while J<=1 *)
429           readIn(DESCRIP_FILE, PRIORITY_DELAY);
430           readIn(DESCRIP_FILE, END_DELAY);
431         end; (* Begin individual device descriptions *)
432         for J:= 1 to 4 do
433         begin

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951 with ADAPTER(I),DEVICE(I) do
952 begin
953   readln(DESCRIP_FILE,RESP);
954   if (RESP[1] = '1') or (RESP[1] = '4') then
955     OPEN:=false
956   else
957     OPEN:=true;
958   if not(OPEN) then
959     begin
960       readln(DESCRIP_FILE,DEV_NUM);
961       D_NUM:=DEV_NUM-DIV_100;
962       readln(DESCRIP_FILE,BUFFER_SIZE);
963       readln(DESCRIP_FILE,TFR_RATE);
964       LOAD_TIME:=1-DATER_RATE;
965       readln(DESCRIP_FILE,NUM_OF_SOURCES);
966       for K:=1 to NUM_OF_SOURCES do
967         begin
968           readln(DESCRIP_FILE,SOURCE(K),TX_RATE);
969           end; (* for K:=1 to num of sources *)
970           TX_INTERVAL:=TX_INTERVAL*ADAPTER(I),DEVICE(I));
971           NEXT TX := TX_INTERVAL;
972           end; (* if not(OPEN) *)
973           end; (* with ADAPTER(I),DEVICE(I) *)
974           end; (* for I:=1 to 4 *)
975           end; (* with ADAPTER(I) *)
976           end; (* for I:=1 to num of adapters *)
977           INITIALIZE PROB MATRIX;
978           end; (* FILE INPUT case *)
979           otherwise : end; (* case *)
980           end; (* procedure CHARACTERIZE NETWORK *)
981           (* ..... *)
982           (* ..... *)
983           (* ..... *)
984           (* ..... *)
985           (* ..... *)
986           (* ..... *)
987           (* ..... *)
988           (* ..... *)
989           (* ..... *)
990           (* ..... *)
991           (* ..... *)
992           (* ..... *)
993           (* ..... *)
994           (* ..... *)
995           (* ..... *)
996           (* ..... *)
997           (* ..... *)
998           (* ..... *)
999           (* ..... *)
1000          (* ..... *)
1001          (* ..... *)
1002          (* ..... *)
1003          (* ..... *)
1004          (* ..... *)
1005          (* ..... *)
1006          (* ..... *)
1007          (* ..... *)
1008          (* ..... *)
1009          (* ..... *)
1010          (* ..... *)
1011          (* ..... *)
1012          (* ..... *)
1013          (* ..... *)
1014          (* ..... *)
1015          (* ..... *)
1016          (* ..... *)
1017          (* ..... *)
1018          (* ..... *)
1019          (* ..... *)
1020          (* ..... *)
1021          (* ..... *)
1022          (* ..... *)
1023          (* ..... *)
1024          (* ..... *)
1025          (* ..... *)
1026          (* ..... *)
1027          (* ..... *)
1028          (* ..... *)
1029          (* ..... *)
1030          (* ..... *)

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```
1031 J:=J+1;
1032 end; (* while j<k *)
1033 if j=1 then j:=j+1;
1034 while (j>1) and (j<=MAX_NUM_ADAPTERS) do
1035   begin
1036     write(AUX_OUT, '4, Propagation distance to adapter ', j:2, ' sec');
1037     write(AUX_OUT, PROP_DIST_10[j]:9:7, ' sec');
1038     LCT:=LCT+1;
1039     j:=j+1;
1040   end; (* while j>1 and j<= MAX_NUM_ADAPTERS *)
1041   write(AUX_OUT);
1042   LCT:=LCT+1;
1043   write(AUX_OUT, '4, Priority delay= ', ' sec');
1044   write(AUX_OUT, PRIORITY_DELAY:9:7, ' sec');
1045   LCT:=LCT+1;
1046   write(AUX_OUT, '4, End delay= ', ' sec');
1047   write(AUX_OUT, END_DELAY:9:7, ' sec');
1048   LCT:=LCT+2;
1049   (* Begin individual device descriptions *)
1050   for j:= 1 to 4 do
1051     begin
1052       with ADAPTER[j], DEVICE[j] do
1053         begin
1054           write(AUX_OUT);
1055           LCT:=LCT+1;
1056           write(AUX_OUT, '4, Device ', j:2);
1057           write(AUX_OUT, '2, status= ', ' sec');
1058           if OPEN then begin write(AUX_OUT, 'OPEN');
1059             LCT:=LCT+1; end
1060           else write(AUX_OUT, 'CLOSED');
1061           if not(OPEN) then
1062             begin
1063               write(AUX_OUT, '2, Device number ', DEV_NUM:2);
1064               LCT:=LCT+1;
1065               write(AUX_OUT, '32, Buffer size= ', ' bytes');
1066               write(AUX_OUT, BUFFER_SIZE DIV 8:8, ' bytes');
1067               LCT:=LCT+1;
1068               write(AUX_OUT, '32, 1/70 bus transfer rate= ');
1069               write(AUX_OUT, TFER_RATE/8:12:2, ' Bps');
1070               LCT:=LCT+1;
1071               write(AUX_OUT, '32, Load time= ', LOAD_TIME:12:2, ' sec');
1072               LCT:=LCT+1;
1073               write(AUX_OUT, '32, Number of offset sources= ');
1074               write(AUX_OUT, NUM_OF_SOURCES:3);
1075               LCT:=LCT+1;
1076               for k:=1 to NUM_OF_SOURCES do
1077                 begin
1078                   write(AUX_OUT, '36, Source #', k:2, ' transmission rate= ');
1079                   write(AUX_OUT, SOURCE[k]:9:2, ' Bps');
1080                   LCT:=LCT+1;
1081                   end; (* for k:=1 to num of sources *)
1082                   write(AUX_OUT, '32, Trunk transmission interval= ', TR_INTRVL:7:4, ' sec');
1083                   LCT:=LCT+1;
1084                   end; (* with ADAPTER *)
1085                   end; (* for j=1 to 4 *)
1086                   end; (* for j=1 to 4 *)
1087                   end; (* for j=1 to 4 *)
1088                   end; (* for j=1 to 4 *)
1089                   end; (* for j=1 to 4 *)
1090                   end; (* for j=1 to 4 *)
1091                   end; (* for j=1 to 4 *)
1092                   end; (* for j=1 to 4 *)
1093                   end; (* for j=1 to 4 *)
1094                   end; (* for j=1 to 4 *)
1095                   end; (* for j=1 to 4 *)
1096                   end; (* for j=1 to 4 *)
1097                   end; (* for j=1 to 4 *)
1098                   end; (* for j=1 to 4 *)
1099                   end; (* for j=1 to 4 *)
1100                   end; (* for j=1 to 4 *)
1101                   end; (* for j=1 to 4 *)
1102                   end; (* for j=1 to 4 *)
1103                   end; (* for j=1 to 4 *)
1104                   end; (* for j=1 to 4 *)
1105                   end; (* for j=1 to 4 *)
1106                   end; (* for j=1 to 4 *)
1107                   end; (* for j=1 to 4 *)
1108                   end; (* for j=1 to 4 *)
1109                   end; (* for j=1 to 4 *)
1110                   end; (* for j=1 to 4 *)
```


writeln(AUX_OUT);
LCT:=LCT+1;

for J:=1 to NUM_OF_ADAPTERS do
for J:=1 to 2 do
with ADAPTER[J].DEVICE[J] do
begin
if not OPEN then

begin
write(AUX_OUT,I:4,J:1);
ROW:=ROW+1;
for K:=1 to D_NUM do

begin
write(AUX_OUT,PR[ROW,K]:7:2);
end;
for K:=1 to D_NUM do
writeln(AUX_OUT);

LCT:=LCT+1;
end;
(* procedure PRINT DESCRIPTION *)

(*****
(*
(* PROCEDURE CREATE DESCRIP FILE;
(*
(* WRITES TO THE AUXILIARY INPUT FILE
(*****

0 A begin (* procedure CREATE *)

rewrite(DESCRIP_FILE);

for I:=1 to NUM_OF_ADAPTERS do

begin
with ADAPTER[I] do

begin
J:=1;

while J<I do

begin
writeln(DESCRIP_FILE,PROP_DIST_TO[J]);

J:=J+1;

end; (* while J<K *)

if J=1 then J:=J+1;

while (J>1) and (J<MAX_NUM_ADAPTERS) do

begin
writeln(DESCRIP_FILE,PROP_DIST_TO[J]);

J:=J+1;
end; (* while J>1 AND <= MAX_NUM_ADAPTERS *)

writeln(DESCRIP_FILE,PRIORITY_DELAY);

writeln(DESCRIP_FILE,END_RELAY);

(* Begin individual device descriptions *)

for J:=1 to 4 do

begin
with ADAPTER[I].DEVICE[J] do

begin

writeln(DESCRIP_FILE,OPEN);

if not(OPEN) then

begin
writeln(DESCRIP_FILE,DEV_NUM);

writeln(DESCRIP_FILE,BUFFER_SIZE);

writeln(DESCRIP_FILE,IFER_RATE);

writeln(DESCRIP_FILE,NUM_OF_SOURCES);

for K:=1 to NUM_OF_SOURCES do

begin
writeln(DESCRIP_FILE,SOURCE[K].IFER_RATE);

end; (* for K:=1 to num of sources *)

end; (* if not(OPEN) *)

end; (* with ADAPTER[I].DEVICE(I) *)

end; (* with ADAPTER(I) *)

end; (* for I:=1 to num of adapters *)

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1191 (* Write the probability matrix *)
1192 for I:=1 to D_NUM do
1193   begin
1194     for J:=1 to D_NUM do
1195       write(descrip_file,PR(I,J));
1196       writeln(descrip_file);
1197     end;
1198   end;
1199 (* procedure CREATE *)
1200 0 A end;
1201
1202 (*****
1203      MAIN PROGRAM
1204 *****)
1205
1206 begin (* main program *)
1207   (* New run initializations *)
1208   for COND1 := NORMAL to OTHER do
1209     CONDITION[COND1] := COND1;
1210   PRINT_ALL := true;
1211   U := 31622 ; (* U is seed for RNG *)
1212   WAIT_TALL := 0;
1213   COLLISION_TALL := 0;
1214   TOTAL_ATTEMPTS := 0;
1215   LCI := 0;
1216   PC := 0;
1217   TIR := 0;
1218   BITS := 0;
1219   write(TERMINAL, "Maximum run time in secs?");
1220   readln(TERMINAL, MAX_TIME);
1221   write(TERMINAL, MAX_TIME);
1222   write(TERMINAL, "Maximum successful transmissions?");
1223   readln(TERMINAL, MAX_TX);
1224   write(TERMINAL, MAX_TX);
1225   write(TERMINAL, "Seed for random number generator?");
1226   readln(TERMINAL, U);
1227   write(TERMINAL, U);
1228   write(TERMINAL, "PRINT_ALL condition on?");
1229   readln(TERMINAL, RESP);
1230   if (RESP[1] = 'Y') or (RESP[1] = 'y') then
1231     PRINT_ALL := true;
1232   else
1233     PRINT_ALL := false;
1234   write(TERMINAL, PRINT_ALL);
1235   write(TERMINAL, "Network cold start conditions");
1236   CHARACTERIZE_NETWORK;
1237   CREATE_OUTPUT;
1238   CREATE_DESCRIPTOR_FILE;
1239   PRINT_DESCRIPTOR;
1240   FIND_NEXT(TMITR);
1241   CURRENT_TIME := TMITR.NEXT_YR;
1242
1243   (* Network steady state operation *)
1244   repeat (* until time exceeds max time *)

```

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```

1471 PICK_A(PCVR,TMITTR);
1472 (* Calculate a collision probability *)
1473
1474 if not A_COLLISION then
1475   begin
1476     TX_TALLY := TX_TALLY+1;
1477     UPDATE_CLOCKS(CONDITION(NORMAL));
1478     if PRINT_ALL then
1479       PRINT_TX_STATS(TMITTR,RCVR);
1480   end
1481   FIND_NEXT(TMITTR);
1482   CURRENT_TIME:=TMITTR.NEXT_TX;
1483 else
1484   begin
1485     COLLISION_TALLY := COLLISION_TALLY +1;
1486     UPDATE_CLOCKS(CONDITION(COLLISION));
1487     if PRINT_ALL then
1488       PRINT_COLLISION_STATS(TMITTR,RCVR);
1489     FIND_NEXT(TMITTR);
1490     CURRENT_TIME:= TMITTR.NEXT_TX;
1491   end; (* if *)
1492   until (CURRENT_TIME >= MAX_TIME) or (TX_TALLY >= MAX_TX);
1493   TOTAL_ATTEMPTS:=TX_TALLY+COLLISION_TALLY;
1494   PRINT_NETWORK_STATS;
1495   ACTIVITY_SUMMARY;
1496   writeln(" END OF RUN");
1497   end. (* main program *)
1498
1499 Compilation complete - no errors found.

```

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```

MAP 1001 HOSC.XQT
MAP 1001 528111 07/01/83 16:19:18
START=020075, PROG SIZE(1/D)=7747/4772
SYSS+RLIBS. LEVEL
END MAP. ERRORS: 0 TIME: 9.310 STORAGE: 12160/3/025777/073777

```

```

@XQT HOSC.XQT
Maximum run time in secs?
Maximum successful transmissions?
Seed for random number generator?
PRINT_ALL condition on?
Input mode: INTERACTIVE or FILE

```

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*** END OF RUN ***
Current time: 36.4361 secs
Successful transmissions: 500
Collisions: 29
Valid: 529
Total attempts: 529
Total time active: 1.7105 secs
Total time active: 4.62%
Total Bytes transmitted: 987360
Seed for RNG: 312577
END OF RUN

BRK.Y

*** END OF RUN NETWORK STATISTICS ***

Current time: 35.4361 secs

Successful transmissions: 520

Collisions: 29

Waits: 51

Total attempts: 529

Total time active: 1.7125225 secs

Total time active: 2.692

Total Bytes transmitted: 987360

Seed for RNG: 312577

DEVICE ACTIVITY SUMMARIES
(SECONDS)

ADP DEV #	TIME TRANSMITTING	TIME WAITING	TIME IN COLLISIONS	TIME RECEIVING	TIME ACTIVE	TRANSMISSION COUNT	RECEPTION COUNT	WAIT COUNT	COLLISION COUNT
1 1	0.9198	0.3897	0.0273	0	1.3357	252	0	39	20
2 1	0.2711	0.4398	0.0045	0	0.7153	35	0	37	3
2 2	0.4324	0.2037	0.0011	0.0012	0.6383	35	2	1	1
3 1	0.0410	0.0110	0.0043	0.6938	0.7500	71	249	3	3
3 2	0.0410	0.0073	0	0.3959	0.3552	71	143	2	0
3 3	0.0052	0.0037	0.0035	0	0.0123	36	0	1	2
4 1	0	0	0	0	0	0	0	0	0
4 2	0	0	0	0.4376	0.4376	0	71	0	0
4 3	0	0	0	0.2711	0.2711	0	35	0	0

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DEVICE ACTIVITY SUMMARIES
(PERCENT)

ADP DEV #	TIME ACTIVE (TRUNK)	TIME TRANSMITTING (TRUNK)	TIME TRANSMITTING (DEVICE)	TIME WAITING (TRUNK)	TIME WAITING (DEVICE)	TIME IN COLLISIONS (TRUNK)	TIME IN COLLISIONS (DEVICE)	TIME IN COLLISIONS (DEVICE)
1 1	78.15	5.27	2.61	22.28	7.15	1.58	0.98	0.98
2 1	41.82	1.95	0.77	25.71	1.19	0.26	0.15	0.15
2 2	25.62	25.28	1.24	0.21	0.37	0.06	0.11	0.11
3 1	43.95	2.40	0.11	0.54	0.71	0.25	0.28	0.28
3 2	20.77	2.40	0.11	0.43	0.71	0	0	0
3 3	0.72	0.20	0.01	0.21	0.37	0.20	0.17	0.17
4 1	0	0.20	0	0.21	0	0.20	0	0
4 2	25.58	0	0	0	0	0	0	0
4 3	15.85	0	0	0	0	0	0	0

NETWORK DESCRIPTION
Adapter # 1

Adapter # 1:
Propagation distance to adapter 2: 0 sec
Propagation distance to adapter 3: 0 sec
Propagation distance to adapter 4: 0 sec

Priority delay: 0.000010 sec
End delay:

Device 1 status: CLOSED Device number 111 2096 bytes
Buffer size: 50000-00 bps
I/O bus transfer rate: 50000-00 bps
Load time: 0.000002 sec
Number of offnet sources: 1
Source # 1 transmission rate: 15000.00 bps
Trunk transmission interval: 0.1337 sec

Device 2 status: OPEN

Device 3 status: OPEN

Device 4 status: OPEN

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NETWORK DESCRIPTION
Adapter # 2

Adapter # 2:
Propagation distance to adapter 1: 0 sec
Propagation distance to adapter 3: 0 sec
Propagation distance to adapter 4: 0 sec
Priority delay: 0 sec
End delay: 0.000020 sec

Device 1 status: CLOSED
Device number 221
Buffer size: 4000 Bytes
I/O bus transfer rate: 500000.00 bps
Load time: 0.000002 sec
Number of offnet sources: 1
Source # 1 transmission rate: 6200.00 bps
Trunk transmission interval: 1.0000 sec

Device 2 status: CLOSED
Device number 322
Buffer size: 6400 Bytes
I/O bus transfer rate: 500000.00 bps
Load time: 0.000002 sec
Number of offnet sources: 1
Source # 1 transmission rate: 6400.00 bps
Trunk transmission interval: 1.0000 sec

Device 3 status: OPEN
Device 4 status: OPEN

NETWORK DESCRIPTION
Adapter # 3

Adapter # 3:
Propagation distance to adapter 1: 0 sec
Propagation distance to adapter 2: 0 sec
Propagation distance to adapter 4: 0 sec

Priority delay: 0 sec
End delay: 0.000000 sec

Device 1 status: CLOSED
Device number 431 512 Bytes
Buffer size: 500000.00 bps
I/O bus transfer rate: 0.000002 sec
Load time: 0.000002 sec
Number of offnet sources: 1
Source # 1 transmission rate: 1000.00 bps
Trunk transmission interval: 0.5120 sec

Device 2 status: CLOSED
Device number 532 512 Bytes
Buffer size: 500000.00 bps
I/O bus transfer rate: 0.000002 sec
Load time: 0.000002 sec
Number of offnet sources: 1
Source # 1 transmission rate: 1000.00 bps
Trunk transmission interval: 0.5120 sec

Device 3 status: CLOSED
Device number 613 624 Bytes
Buffer size: 3300000.00 bps
I/O bus transfer rate: 0.000000 sec
Load time: 0.000000 sec
Number of offnet sources: 1
Source # 1 transmission rate: 625.00 bps
Trunk transmission interval: 0.0984 sec

Device 4 status: OPEN

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NETWORK DESCRIPTION
Adapter # 4

Adapter # 4:
Propagation distance to adapter 1: 0 sec
Propagation distance to adapter 2: 0 sec
Propagation distance to adapter 3: 0 sec

Priority delay: 0.000000 sec
End delay: 0.000000 sec

Device 1 status: CLOSED Device number 741 2000 Bytes
Buffer size: 100 bus transfer rate: 1000000.00 bps
Load time: 0.000000 sec
Number of offnet sources: 0
Trunk transmission interval: 1000000.000000 sec

Device 2 status: CLOSED Device number 842 2000 Bytes
Buffer size: 100 bus transfer rate: 1000000.00 bps
Load time: 0.000000 sec
Number of offnet sources: 0
Trunk transmission interval: 1000000.000000 sec

Device 3 status: CLOSED Device number 943 2000 Bytes
Buffer size: 100 bus transfer rate: 1000000.00 bps
Load time: 0.000000 sec
Number of offnet sources: 0
Trunk transmission interval: 1000000.000000 sec

Device 4 status: OPEN

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CUMULATIVE PROBABILITY OF TRANSITION MATRIX

[illegible]